



AOS
SEMICONDUCTOR

产品规格说明书

Product Data Sheet

AOS2252FXx

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电源管理IC



通信接口芯片



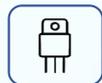
二三极管



LDO稳压器



逻辑器件



MOSFETs



运算放大器



显示驱动



MCU单片机



光电器件



DESCRIPTIONS

The AOS2252F is a CMOS analog IC configured as two 4-channel multiplexers. This CMOS device can operate from 2.5 V to 5.5 V.

The AOS2252F device are digitally-controlled analog switches. It has low on-resistance (53 TYP) and very low off-leakage current (1nA TYP).

The AOS2252F is available in Green SOP16, TSSOP16and QFN3X3-16 packages. It operates over an ambient temperature range of -40°C to +125°C.

FEATURES

- 3dB Bandwidth: 200MHz
- Single Supply Operation: +2.5V to +5.5V
- Low ON Resistance: 53 (TYP) With 5V Supply
- High Off-Isolation: -75dB (RL = 50 , f = 1MHz)
- Break-Before-Make Switching
- Binary Address Decoding on Chip
- Operating Temperature Range: -40°C to +125°C
- PACKAGES: SOP16, TSSOP16 and QFN3X3-16

APPLICATIONS

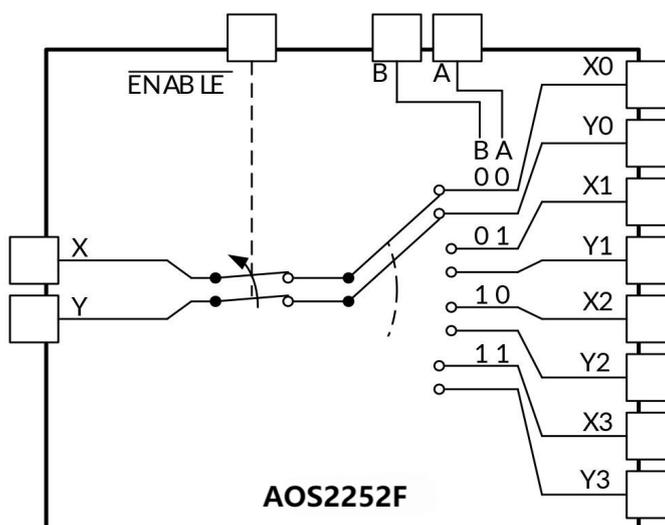
- Sensors
- Analog and Digital Multiplexing and Demultiplexing
- A/D and D/A Conversion
- Signal Gating
- Battery-Operated Equipment
- Factory Automation
- Appliances
- Communications Circuits

Device Information (1)

PART NUMBER	PACKAGE	BODY SIZE (NOM)
AOS2252F	SOP16	9.90mm x 3.91mm
	TSSOP16	5.00mm x 4.40mm
	QFN3X3-16	3.00mm x 3.00mm

(1)For all available packages, see the orderable addendum at the end of the data sheet.

Functional Diagrams of AOS2252F



**PACKAGE/ORDERING INFORMATION⁽¹⁾**

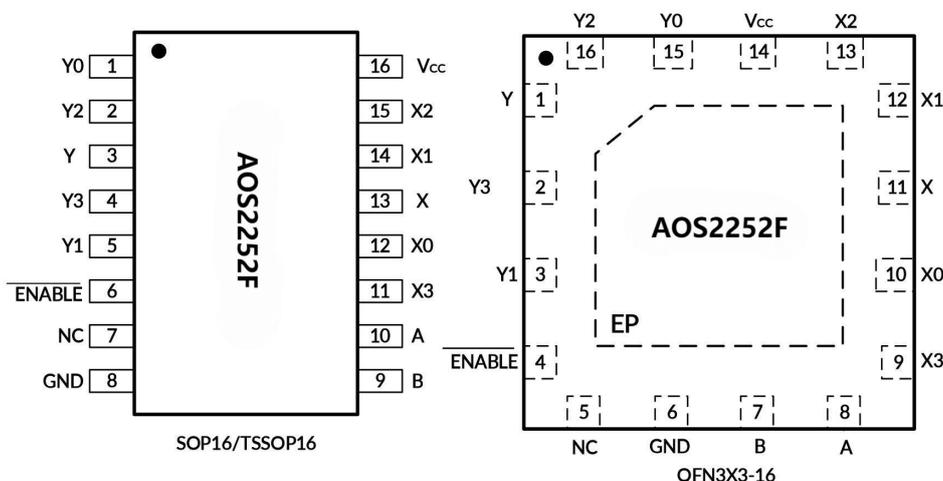
PRODUCT	ORDERING NUMBER	TEMPERATURE RANGE	PACKAGE LEAD	PACKAGE MARKING ⁽²⁾	PACKAGE OPTION
AOS2252F	AOS2252FXS16	-40°C ~ +125°C	SOP16	AOS2252	Tape and Reel , 4000
	AOS2252FXTSS16	-40°C ~ +125°C	TSSOP16	AOS2252	Tape and Reel , 4000
	AOS2252FXTQC16	-40°C ~ +125°C	QFN3X3-16	AOS2252	Tape and Reel , 5000

NOTE:

- (1) This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the right-hand navigation.
- (2) There may be additional marking, which relates to the lot trace code information (data code and vendor code), the logo or the environmental category on the device.



Pin Configurations (Top View)



PIN DESCRIPTION

NAME	PIN		FUNCTION
	SOP16/TSSOP16	QFN3X3-16	
X0-X3	12, 14, 15, 11	10, 12, 13, 9	Analog Switch Inputs X0-X3.
Y0-Y3	1, 5, 2, 4	15, 3, 16, 2	Analog Switch Inputs Y0-Y3.
X	13	11	Analog Switch "X" Output.
Y	3	1	Analog Switch "Y" Output.
vcc	16	14	Positive Analog and Digital Supply Voltage Input.
A	10	8	Digital Address "A" Input.
B	9	7	Digital Address "B" Input.
GND	8	6	Ground. Connect to digital ground.
NC	7	5	No Connect.
- ENABLE	6	4	Digital Enable Input. Normally connected to GND.
EP	-	ExposedPad	Exposed Pad. Connect EP to GND.

FUNCTION TABLE

- INPUT	INPUT STATES		ON CHANNEL(S)
	B	A	
1	X	X	NONE
0	0	0	X0, Y0
0	0	1	X1, Y1
0	1	0	X2, Y2
0	1	1	X3, Y3

X=Don't care

NOTE: Input and output pins are identical and inter-changeable. Either may be considered an input or output; signals pass equally well in either direction.



SPECIFICATIONS

Absolute Maximum Ratings

Over operating free-air temperature range (unless otherwise noted)⁽¹⁾

SYMBOL	PARAMETER		MIN	MAX	UNIT
V _{CC}	Supply Voltage		-0.3	6	V
V _{IN}	Input Voltage (All inputs)		-0.3	V _{CC} +0.3	
I _{IN}	Switch Input Current	Any one input	-20	20	mA
I _{PEAK}	Peak Switch Current	Pulsed at 1ms Duration, <10% Duty Cycle	-40	40	
J _A	Package thermal impedance ⁽²⁾	SOP16		150	°C/W
		TSSOP16		45	
		QFN3X3-16		70	
T _J	Junction Temperature ⁽³⁾		-40	150	°C
T _{stg}	Storage temperature		-65	150	

- (1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those specified is not implied.
- (2) The package thermal impedance is calculated in accordance with JESD-51.
- (3) The maximum power dissipation is a function of T_{J(MAX)}, R_{JA}, and T_A. The maximum allowable power dissipation at any ambient temperature is P_D = (T_{J(MAX)} - T_A) / R_{JA}. All numbers apply for packages soldered directly onto a PCB.



ESD Ratings

The following ESD information is provided for handling of ESD-sensitive devices in an ESD protected area only.

			VALUE	UNIT
$V_{(ESD)}$	Electrostatic discharge	Human-body model (HBM)	± 3000	V
		Charged-device model (CDM)	± 1000	V
		Machine Model (MM)	± 200	V



ESD SENSITIVITY CAUTION

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.



Recommended Operating Conditions

Over operating free-air temperature range (unless otherwise noted).

SYMBOL	PARAMETER	MIN	MAX	UNIT
V _{CC}	Supply Voltage	2.5	5.5	V
T _A	Operating temperature	-40	125	°C



Electrical Characteristics

$V_{CC} = 5.0\text{ V}$ or 3.3 V , FULL= -40°C to $+125^{\circ}\text{C}$, Typical values are at $T_A = +25^{\circ}\text{C}$ (unless otherwise noted).

PARAMETER	SYMBOL	CONDITIONS	V_{CC}	T_A	MIN	TYP	MAX	UNIT
ANALOG SWITCH								
Analog Signal Range	V_{X-}, V_{Y-}			FULL	GND		V_{CC}	V
On-Resistance	R_{ON}	$V_{CC}=5\text{V}, I_X, I_Y=1\text{mA}$	5V	$+25^{\circ}\text{C}$		53	60	
				FULL			77	
		$V_{CC}=3.3\text{V}, I_X, I_Y=1\text{mA}$	3.3V	$+25^{\circ}\text{C}$		102	120	
				FULL			140	
On-Resistance Match Between Channels	$R_{ON}^{(3)}$	$V_{CC}=5\text{V}, I_X, I_Y=1\text{mA}$ Switch ON	5V	$+25^{\circ}\text{C}$		1.5	5	
				FULL			5.3	
On-Resistance Flatness	$R_{FLAT(ON)}^{(4)}$	$V_{CC}=5\text{V}, I_X, I_Y=1\text{mA}$ Switch ON	5V	$+25^{\circ}\text{C}$		23	27	
				FULL			40	
X _{Off} , Y _{Off} , X _{On} , Y _{On} Leakage Current	$I_{X(OFF)}, I_{Y(OFF)}, I_{X(ON)}, I_{Y(ON)}$	$V_{CC}=5\text{V}, V_{X-}, V_{Y-}=1\text{V}, 4.5\text{V}$ $V_X, V_Y=4.5\text{V}, 1\text{V}$	5V	FULL		1	1000	nA
								$V_{CC}=3.3\text{V}, V_{X-}, V_{Y-}=1\text{V}, 3\text{V}$ $V_X, V_Y=3\text{V}, 1\text{V}$
DIGITAL CONTROL INPUTS⁽⁵⁾								
Logic Input Logic Threshold High	$V_{AH}, V_{BH}, V_{-ENABLE}$		5V	FULL	1.8			V
			3.3V	FULL	1.5			V
Logic Input Logic Threshold Low	$V_{AL}, V_{BL}, V_{-ENABLE}$		5V	FULL			0.7	V
			3.3V	FULL			0.5	V
Input-Current High	$I_{AH}, I_{BH}, I_{-ENABLE}$	$V_A, V_B, V_{-ENABLE} = V_{CC}$	3.3V to 5V	$+25^{\circ}\text{C}$		1	1000	nA
				FULL			1000	
Input-Current Low	$I_{AL}, I_{BL}, I_{-ENABLE}$	$V_A, V_B, V_{-ENABLE} = 0\text{V}$	3.3V to 5V	$+25^{\circ}\text{C}$		1	1000	nA
				FULL			1000	

- (1) Limits are 100% production tested at 25°C. Limits over the operating temperature range are ensured through correlations using statistical quality control (SQC) method.
- (2) Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration.
- (3) This parameter is ensured by design and/or characterization and is not tested in production.
- (4) Flatness is defined as the difference between the maximum and minimum values of ON-state resistance over the specified range of conditions.
- (5) All unused digital inputs of the device must be held at V_{CC} or GND to ensure proper device operation.



Electrical Characteristics(continued)

V_{CC} = 5.0 V or 3.3V, FULL= -40°C to +125°C, Typical values are at T_A= +25°C (unless otherwise noted).

PARAMETER	SYMBOL	CONDITIONS	V _{CC}	T _A	MIN	TYP	MAX	UNIT
DYNAMIC CHARACTERISTICS								
Propagation Delay	t _{pd}	R _L = 300 Ω, C _L = 35pF, See Figure 2	5V	+25°C		6		ns
			3.3V	+25°C		8		ns
Address Transition Time	t _{TRANS}	V _{X-} , V _{Y-} = 3V/0V, R _L = 300 Ω, C _L =35pF, See Figure 3	5V	+25°C		95		ns
			3.3V	+25°C		130		ns
ENABLE Turn-On Time	t _{ON}	V _{X-} , V _{Y-} = 3V, R _L = 300 Ω, C _L = 35pF, See Figure 4	5V	+25°C		70		ns
			3.3V	+25°C		90		
ENABLE Turn-Off Time	t _{OFF}	V _{X-} , V _{Y-} = 3V, R _L = 300 Ω, C _L = 35pF, See Figure 4	5V	+25°C		100		ns
			3.3V	+25°C		115		
Break-Before-Make Time Delay	t _D	V _{X-} , V _{Y-} = 3V, R _L = 300 Ω, C _L = 35pF, See Figure 5	5V	+25°C		60		ns
			3.3V			70		ns
Charge Injection	Q	R _s = 0 Ω, C _L = 1nF, See Figure 6	5V	+25°C		3		pC
			3.3V			2.5		pC
Off Isolation	O _{ISO}	R _L =50 Ω, f=1MHz, See Figure 7	5V	+25°C		-75		dB
Crosstalk	X _{TALK}	f = 1MHz, See Figure 8	5V	+25°C		-83		dB
-3dB Bandwidth	BW	R _L = 50 Ω, See Figure 9	5V	+25°C		200		MHz
			3.3V			200		MHz
Input Off-Capacitance	C _{X(OFF)} C _{Y(OFF)}	f = 1MHz, See Figure 10	5V	+25°C		3.5		pF
Output Off-Capacitance	C _{X(OFF)} C _{Y(OFF)}	f = 1MHz, See Figure 10	5V	+25°C		10		pF
Output On-Capacitance	C _{X(ON)} C _{Y(ON)}	f = 1MHz, See Figure 10	5V	+25°C		13		pF
Total Harmonic Distortion	THD	R _L = 600 Ω, 4.5V _{P-P} , f = 20Hz to 20kHz	5V	+25°C		1.5		%
POWER REQUIREMENTS								
Power Supply Range	V _{CC}			FULL	2.5		5.5	V
Power Supply Current	I _{CC}	V _{CC} = 5.0V, V _A , V _B , V _{-ENABLE} = V _{CC} or 0	5V	FULL		0.001	6	uA
		V _{CC} = 3.3V, V _A , V _B , V _{-ENABLE} = V _{CC} or 0	3.3V	FULL		0.001	3	uA



TYPICAL CHARACTERISTICS

NOTE: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only.

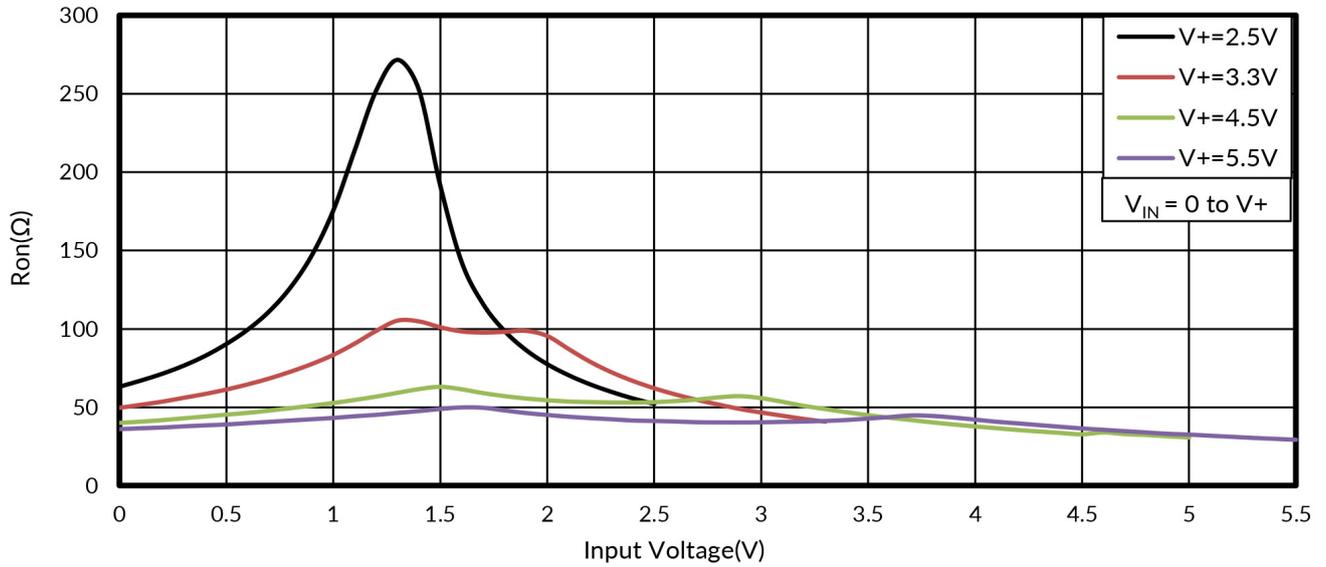


Figure 1. Typical Ron as a Function of Input Voltage



Parameter Measurement Information

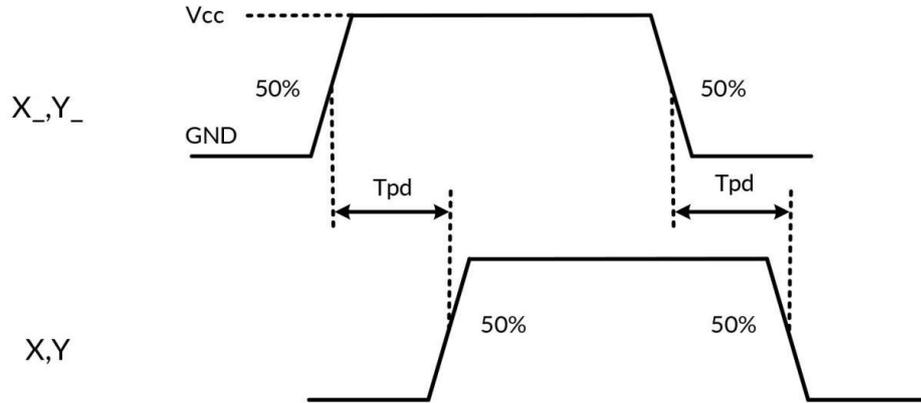


Figure 2. Propagation Delay (t_{pd})

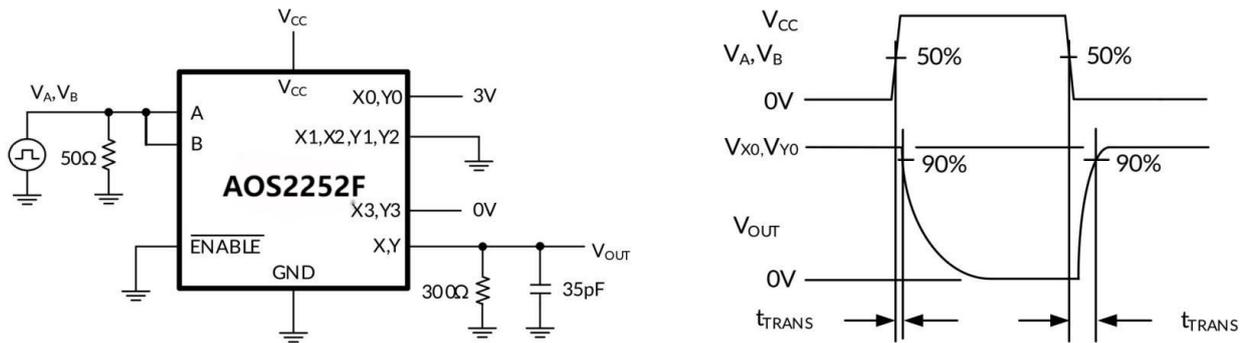


Figure 3. Address Transition Times (t_{TRANS})

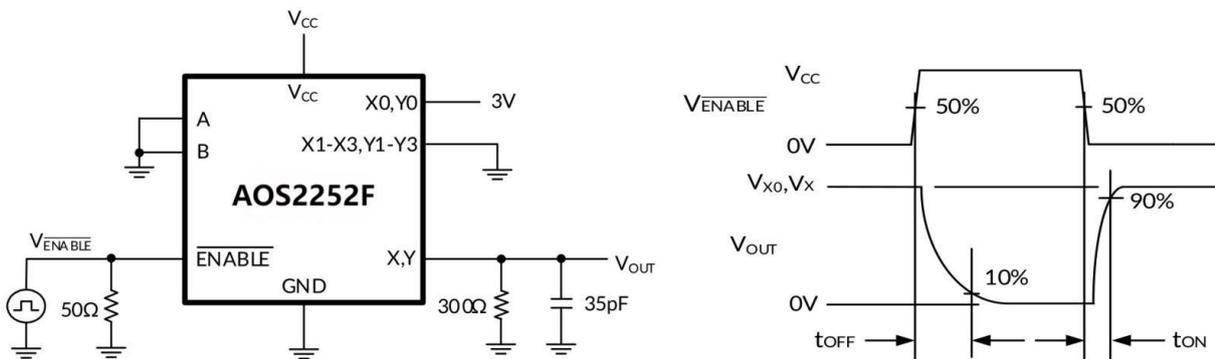


Figure 4. Switching Times (t_{ON} , t_{OFF})



Parameter Measurement Information(continued)

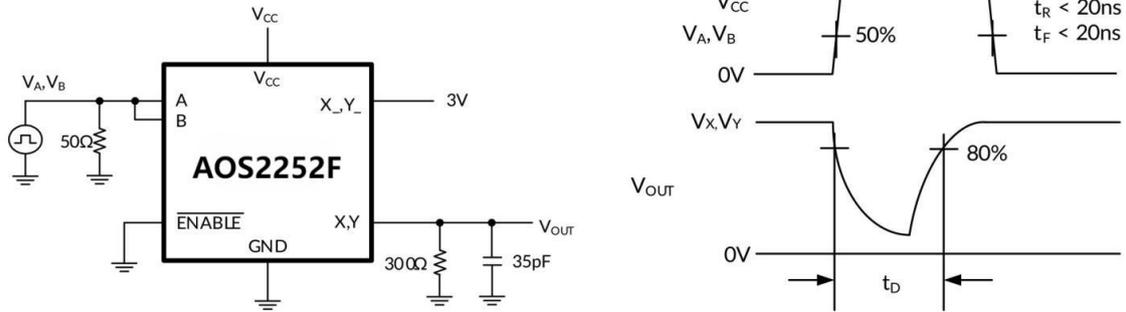


Figure 5. Break-Before-Make Time Delay (t_D)

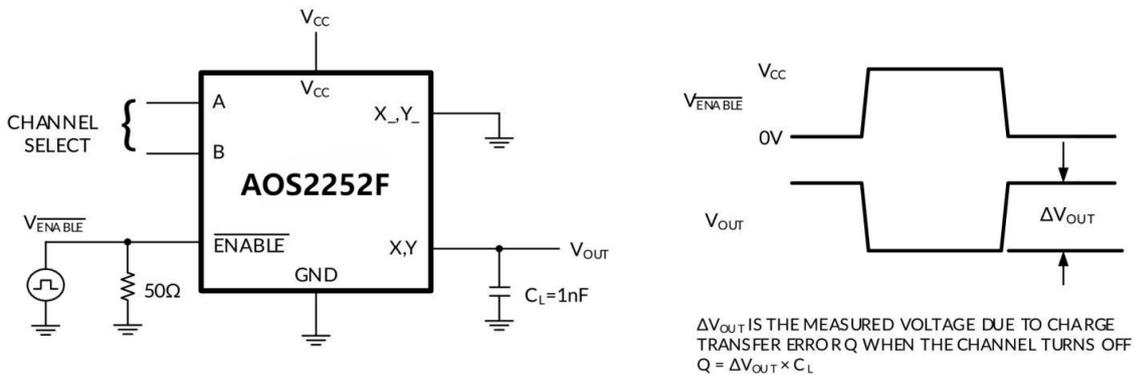


Figure 6. Charge Injection (Q)

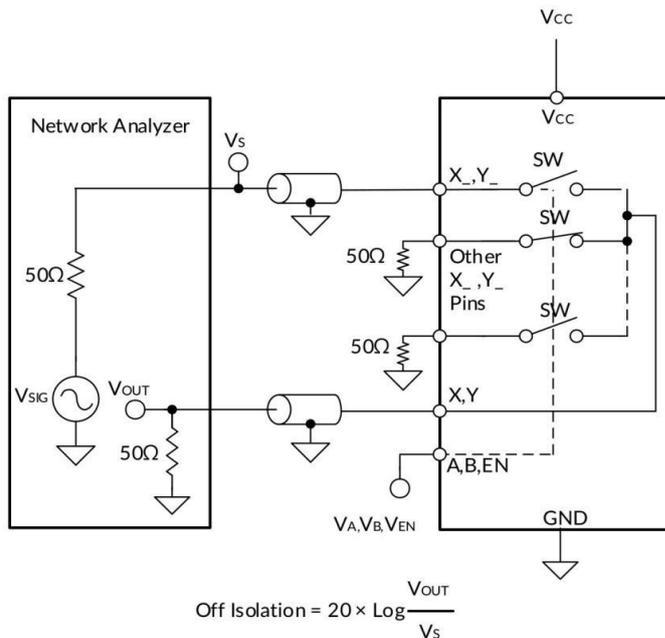


Figure 7. Off Isolation Measurement Setup



Parameter Measurement Information(continued)

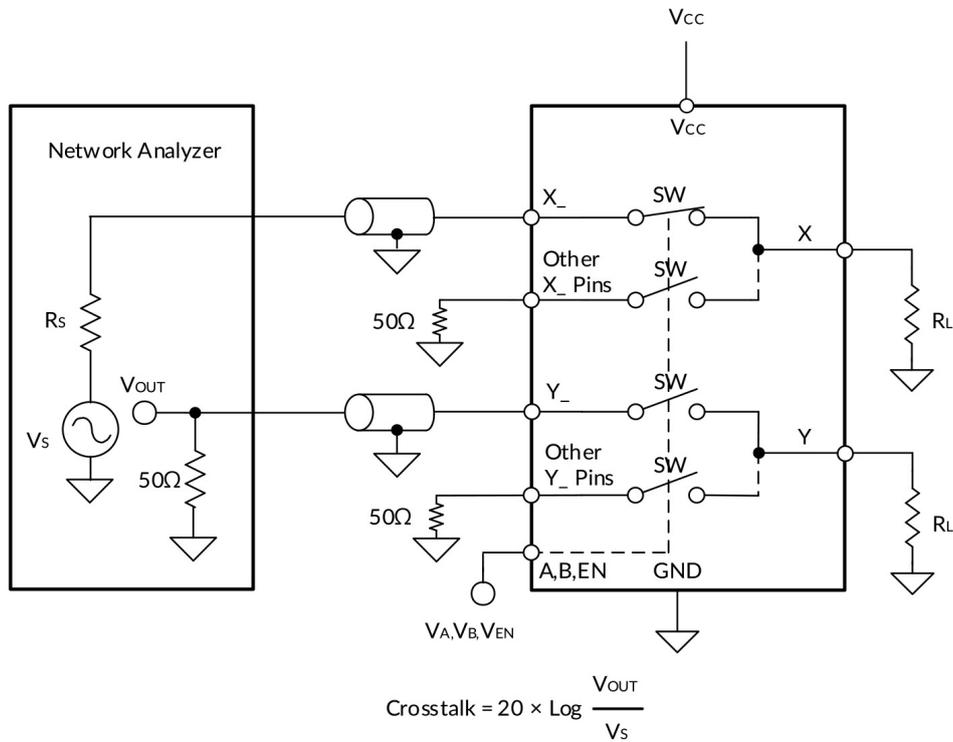


Figure 8. Crosstalk Measurement Setup

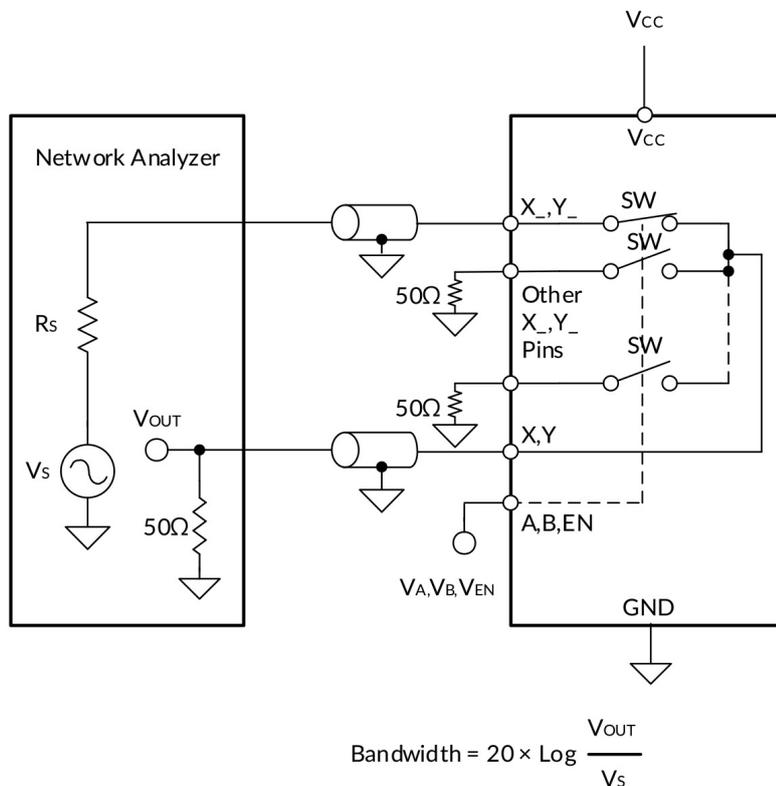


Figure 9. Bandwidth Measurement Setup



Parameter Measurement Information(continued)

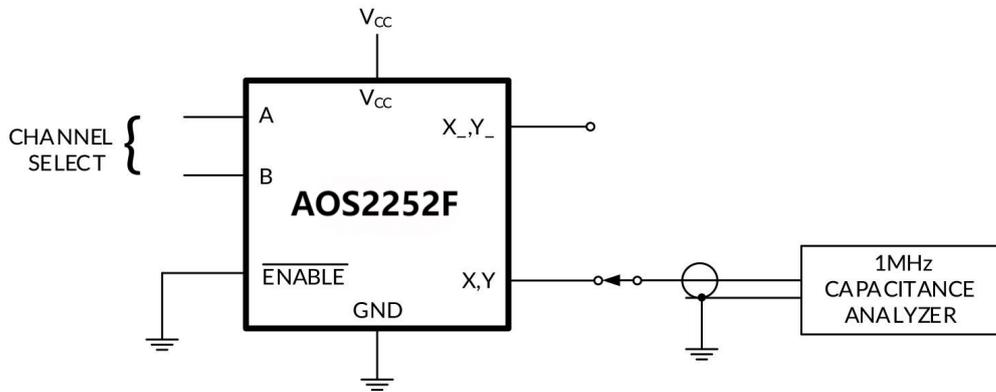


Figure 10. Capacitance



APPLICATION NOTES

The AOS2252F device is a differential 4-channel multiplexer having two binary control inputs, A and B, and an inhibit input. The two binary input signals select 1 of 4 pairs of channels to be turned on and connect the analog inputs to the outputs.

One application of the AOS2252F is to use it in conjunction with a microcontroller to poll a keypad. Figure 11 shows the basic schematic for such a polling system. The microcontroller uses the channel select pins to cycle through the different channels while reading the input to see if a user is pressing any of the keys. This is a very robust setup, allowing for multiple simultaneous key-presses with very little power consumption. It also utilizes very few pins on the microcontroller. The down side of polling is that the microcontroller must continually scan the keys for a press and can do little else during this process.

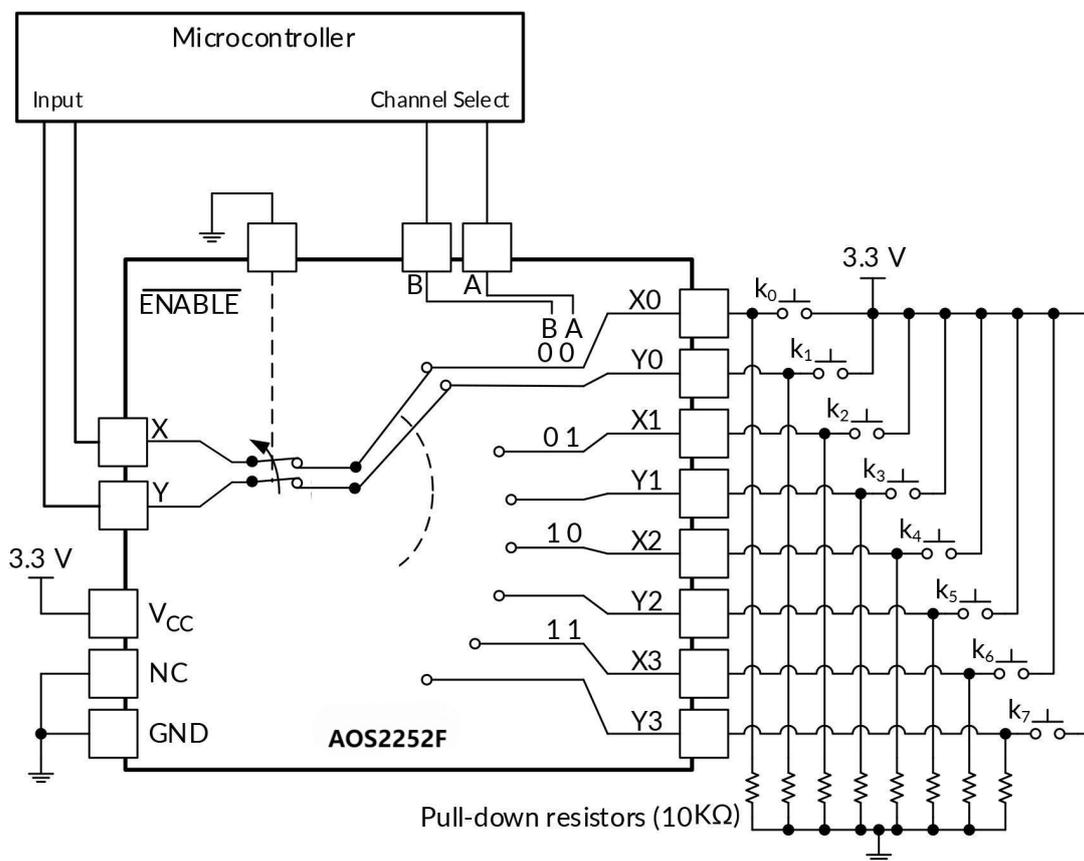
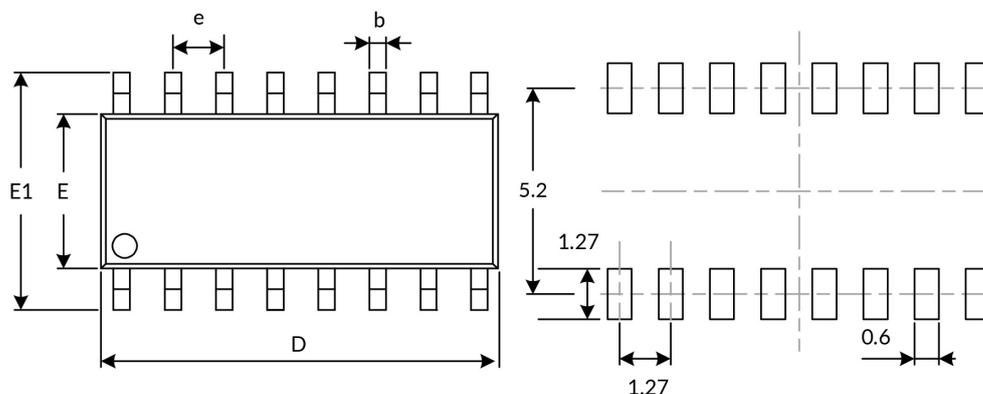


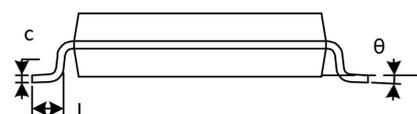
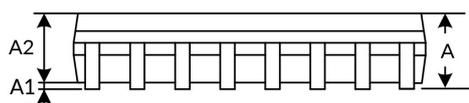
Figure 11. The AOS2252F Being Used to Help Read Button Presses on a Keypad.



PACKAGE OUTLINE DIMENSIONS
SOP16⁽³⁾



RECOMMENDED LAND PATTERN (Unit: mm)



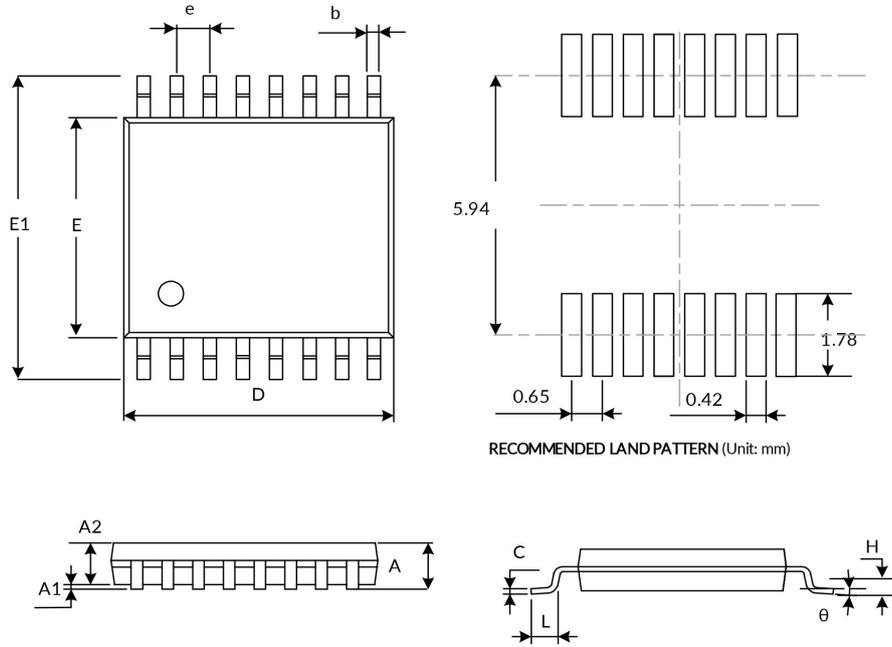
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A ⁽¹⁾	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D ⁽¹⁾	9.800	10.200	0.386	0.402
E ⁽¹⁾	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.27 (BSC) ⁽²⁾		0.050 (BSC) ⁽²⁾	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

NOTE:

1. Plastic or metal protrusions of 0.15mm maximum per side are not included.
2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
3. This drawing is subject to change without notice.



TSSOP16⁽³⁾



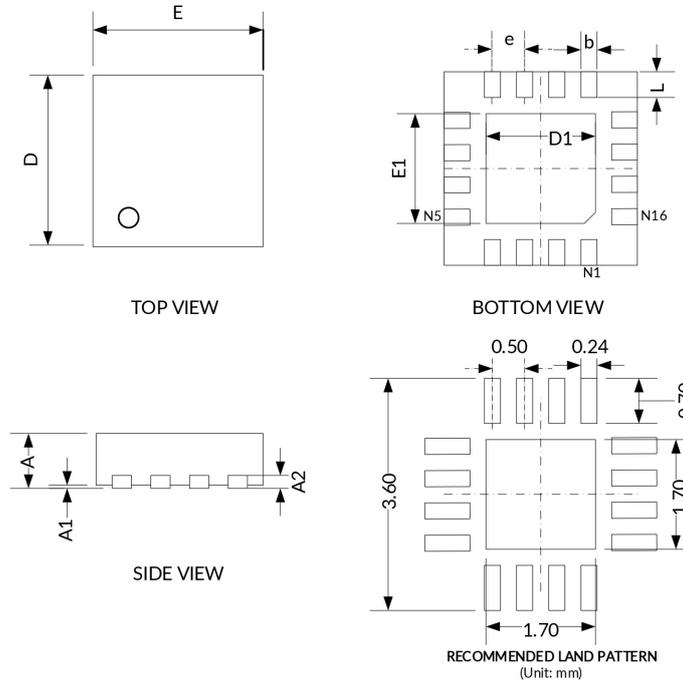
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A ⁽¹⁾		1.200		0.047
A1	0.050	0.150	0.002	0.006
A2	0.800	1.050	0.031	0.041
b	0.190	0.300	0.007	0.012
c	0.090	0.200	0.004	0.008
D ⁽¹⁾	4.860	5.100	0.191	0.201
E ⁽¹⁾	4.300	4.500	0.169	0.177
E1	6.200	6.600	0.244	0.260
e	0.650 (BSC) ⁽²⁾		0.026 (BSC) ⁽²⁾	
L	0.500	0.700	0.02	0.028
H	0.25TYP		0.01TYP	
θ	1°	7°	1°	7°

NOTE:

1. Plastic or metal protrusions of 0.15mm maximum per side are not included.
2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
3. This drawing is subject to change without notice.



QFN3X3-16⁽²⁾



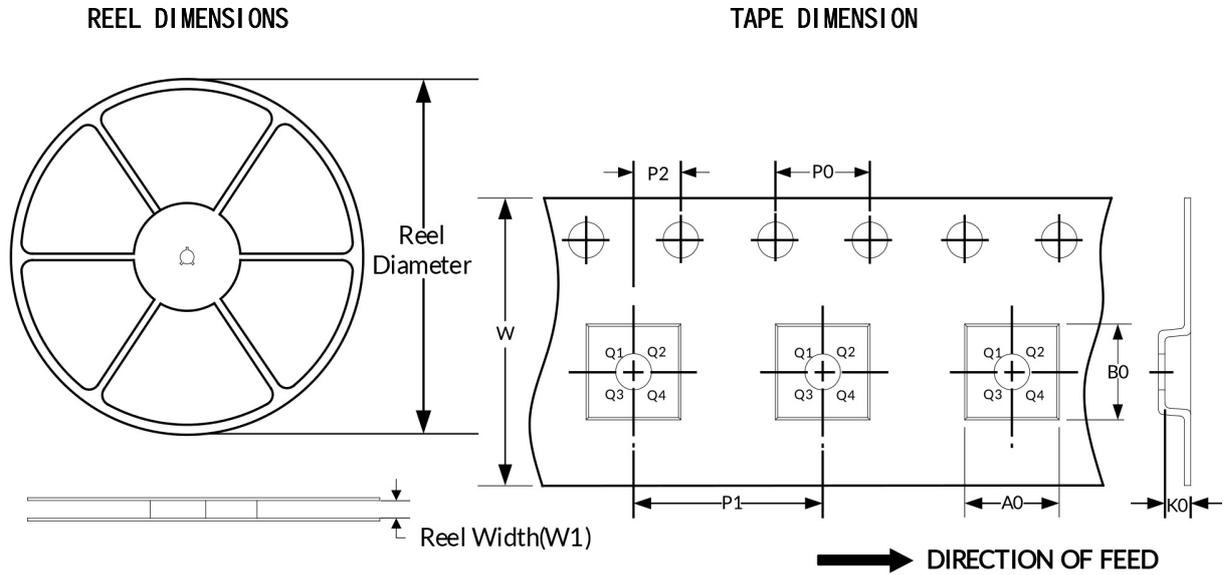
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A ⁽¹⁾	0.700	0.800	0.028	0.031
A1	0.000	0.050	0.000	0.002
A2	0.203		0.008	
b	0.180	0.300	0.007	0.012
D ⁽¹⁾	2.900	3.100	0.114	0.122
D1	1.600	1.800	0.063	0.071
E ⁽¹⁾	2.900	3.100	0.114	0.122
E1	1.600	1.800	0.063	0.071
e	0.500 TYP		0.020 TYP	
L	0.300	0.500	0.012	0.020

NOTE:

1. Plastic or metal protrusions of 0.075mm maximum per side are not included.
2. This drawing is subject to change without notice.



TAPE AND REEL INFORMATION



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width(mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOP16	13''	16.4	6.50	10.30	2.10	4.0	8.0	2.0	16.0	Q1
TSSOP16	13''	12.4	6.90	5.60	1.20	4.0	8.0	2.0	12.0	Q1
QFN3X3-16	13''	12.4	3.35	3.35	1.13	4.0	8.0	2.0	12.0	Q1

NOTE:

1. All dimensions are nominal.
2. Plastic or metal protrusions of 0.15mm maximum per side are not included.