



AOS
SEMICONDUCTOR

产品规格说明书

Product Data Sheet

AOS181xXH

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电源管理IC



通信接口芯片



二三极管



LDO稳压器



逻辑器件



MOSFETs



运算放大器



显示驱动



MCU单片机



光电器件



DESCRIPTIONS

The AOS181 series of voltage output, current shunt monitors (also called current-sense amplifiers) can sense drops across shunts at common-mode voltages from -0.1V to 30V, independent of the supply voltage.

Three fixed gains are available: 20V/V, 50V/V, 100V/V. The device integrate a matched resistor gain network, this matched gain resistor network minimizes gain error and reduces the temperature drift.

These devices operate from a single 3V to 5.5V power supply, drawing a maximum of 270 μ A of supply current. The AOS181 families of operational amplifiers are specified at the full temperature range of -40 $^{\circ}$ C to +125 $^{\circ}$ C, and offered in SOT23-6 package.

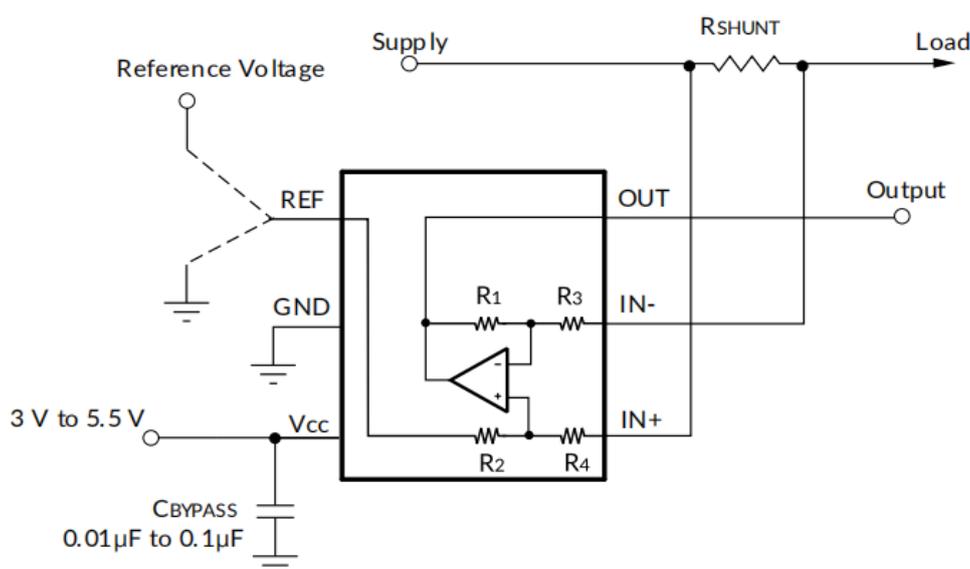
FEATURES

- Wide Common-Mode Range: -0.1V to 30V
- Input Offset Voltage: $\pm 50\mu$ V (Typical)
- High Bandwidth: 400kHz (AOS181A)
- Gain Error: $\pm 1\%$ (Maximum)
- Choice of Gains:
 - AOS181A Gain: 20V/V
 - AOS181B Gain: 50V/V
 - AOS181C Gain: 100V/V
- Quiescent Current: 210 μ A
- Supply Range: 3V to 5.5V
- 40 $^{\circ}$ C to +125 $^{\circ}$ C Operating Temperature Range
- Available in a Green SOT23-6 Package

APPLICATIONS

- Motor Control
- Power Monitoring
- Charging Station
- Solar Inverters
- Precision Current Sources

TYPICAL APPLICATION CIRCUIT





PACKAGE/ORDERING INFORMATION

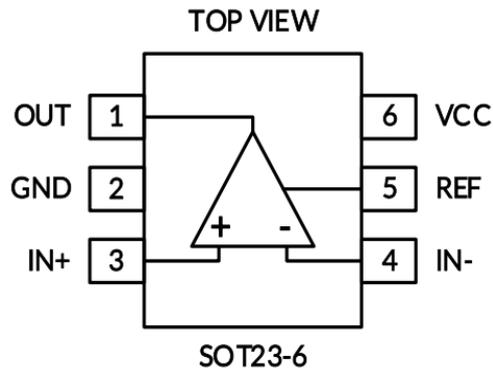
Orderable Device	Package Type	Pin	Channel	Op Temp()	Device Marking ⁽²⁾	MSL ⁽³⁾	Package Qty
AOS181AXH	SOT23-6	6	1	-40 ~125	181A	MSL3	Tape and Reel , 3000
AOS181BXH	SOT23-6	6	1	-40 ~125	181B	MSL3	Tape and Reel , 3000
AOS181CXH	SOT23-6	6	1	-40 ~125	181C	MSL3	Tape and Reel , 3000

NOTE:

- (1) This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the right-hand navigation.
- (2) There may be additional marking, which relates to the lot trace code information (data code and vendor code), the logo or the environmental category on the device.
- (3) AOS classify the MSL level with using the common preconditioning setting in our assembly factory conforming to the JEDEC industrial standard J-STD-20F. Please align with AOS if your end application is quite critical to the preconditioning setting or if you have special requirement.



PIN CONFIGURATION AND FUNCTIONS



Pin Description

NAME	PIN	I/O ⁽¹⁾	DESCRIPTION
	SOT23-6		
REF	5	I	Reference voltage
GND	2	-	Ground
VCC	6	-	Positive power supply, 3V to 5.5V
IN+	3	I	Positive (noninverting) input
IN-	4	I	Negative (inverting) input
OUT	1	O	Output

(1) I = Input, O = Output.



SPECIFICATIONS

Absolute Maximum Ratings

Over operating free-air temperature range (unless otherwise noted) ⁽¹⁾

			MIN	MAX	UNIT
Supply voltage, V_S				6	V
Analog inputs, IN_+ , IN_- ⁽²⁾	Differential (V_{IN+}) - (V_{IN-})		-32	32	
	Common-mode ⁽³⁾		GND-0.3	32	
Input voltage range	at REF pin		GND-0.3	$V_S+0.3$	
Output voltage			GND-0.3	$V_S+0.3$	
Maximum output current, I_{OUT}				8	mA
J_A	Package thermal impedance ⁽⁴⁾	SOT23-6		230	/W
Temperature	Operating range, T_A		-40	125	
	Junction, T_J ⁽⁵⁾			150	
	Storage, T_{Stg}		-65	150	

- (1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- (2) V_{IN+} and V_{IN-} are the voltages at the IN_+ and IN_- pins, respectively.
- (3) Input voltage at any pin can exceed the voltage shown if the current at that pin is limited to 5 mA.
- (4) The package thermal impedance is calculated in accordance with JESD-51.
- (5) The maximum power dissipation is a function of $T_{J(MAX)}$, R_{JA} , and T_A . The maximum allowable power dissipation at any ambient temperature is $P_D = (T_{J(MAX)} - T_A) / R_{JA}$. All numbers apply for packages soldered directly onto a PCB.



ESD Ratings

The following ESD information is provided for handling of ESD-sensitive devices in an ESD protected area only.

			VALUE	UNIT
V _(ESD)	Electrostatic discharge	Human-Body Model (HBM), MIL-STD-883K METHOD 3015.9	± 2000	V
		Charged-Device Model (CDM), ANSI/ESDA/JEDEC JS-002-2022	± 1500	



Pin Description

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

Recommended Operating Conditions

Over operating free-air temperature range (unless otherwise noted)

		MIN	NOM	MAX	UNIT
V _{CM}	Common-mode input voltage(IN+ and IN-)	-0.1	12	30	V
V _S	Operating supply voltage	3	5	5.5	V
T _A	Operating free-air temperature	-40		125	



ELECTRICAL CHARACTERISTICS

(At $T_A = +25^\circ\text{C}$, $V_S = 5\text{V}$, $V_{REF} = V_S/2$, $V_{IN+} = 12\text{V}$, and $V_{SENSE} = V_{IN+} - V_{IN-}$, Full ⁽⁷⁾ = -40°C to $+125^\circ\text{C}$ (unless otherwise noted.)⁽¹⁾

PARAMETER		CONDITIONS	TA	MIN ⁽²⁾	TYP ⁽³⁾	MAX ⁽²⁾	UNIT
INPUT							
CMRR	Common-Mode Rejection Ratio, RTI ⁽⁴⁾	$V_{IN+} = 5\text{V to } 30\text{V}$, $V_{SENSE} = 0\text{mV}$	Full	95	117		dB
V _{OS}	Offset Voltage, RTI	$V_{SENSE} = 0\text{ mV}$	25	-130	± 50	130	μV
		$V_{SENSE} = 0\text{ mV}$, $V_{IN+} = 0\text{ V}$	25	-130	± 50	130	μV
d _{VOS} /d _T	Offset Drift, RTI	$V_{SENSE} = 0\text{ mV}$	Full		0.5		μV/
PSRR	Power-Supply Rejection Ratio, RTI	$V_S = 3\text{V to } 5.5\text{ V}$, $V_{IN+} = 12\text{ V}$, $V_{SENSE} = 0\text{ mV}$	25		± 3	± 8	μV/V
I _{IB}	Input Bias Current	$V_{SENSE} = 0\text{ mV}$, $V_{IN+} = 0\text{ V}$	25		-5		μA
		$V_{SENSE} = 0\text{ mV}$	25		76	100	μA
I _{IO}	Input Offset Current ⁽⁵⁾	$V_{SENSE} = 0\text{ mV}$	25		± 0.05		μA
OUTPUT							
G	Gain	A devices	25		20		V/V
		B devices			50		
		C devices			100		
E _G	Gain Error	$V_{OUT} = 0.5\text{ V to } V_S - 0.5\text{ V}$	Full		± 0.15%	± 1%	
	Gain Error vs Temperature		Full		4.5	20	ppm/
	Nonlinearity Error	$V_{OUT} = 0.5\text{ V to } V_S - 0.5\text{ V}$	25		± 0.025%		
	Maximum Capacitive Load	No sustained oscillation	25		1		nF
VOLTAGE OUTPUT							
V _{SP}	Swing to V _S Power-Supply Rail	$R_L = 10\text{k to } V_S/2$	Full		(V _S)-0.01	(V _S)-0.02	V
V _{SN}	Swing to GND	$R_L = 10\text{k to } V_S/2$	Full		(V _{GND}) ⁺ 0.01	(V _{GND}) ⁺ 0.02	V



FREQUENCY RESPONSE							
BW	Bandwidth	A devices, $C_{LOAD}= 10\text{ pF}$	25		400		kHz
		B devices, $C_{LOAD}= 10\text{ pF}$			185		
		C devices, $C_{LOAD}= 10\text{ pF}$			165		
SR	Slew Rate ⁽⁶⁾	$R_L= 10k, C_{LOAD}= 10\text{ pF}$	25		2		V/ μ s
NOISE, RTI ⁽⁴⁾							
	Voltage Noise Density		25		35		nV/ Hz
	0.1Hz to 10Hz		25		0.9		μ V _{PP}
POWER SUPPLY							
I_o	Quiescent Current	$V_{SENSE}= 0\text{ mV}$	25		210	270	μ A
			Full			280	μ A

NOTE:

- (1) Electrical table values apply only for factory testing conditions at the temperature indicated. Factory testing conditions result in very limited self-heating of the device.
- (2) Limits are 100% production tested at 25°C. Limits over the operating temperature range are ensured through correlations using statistical quality control (SQC) method.
- (3) Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration.
- (4) RTI = referred-to-input
- (5) This parameter is ensured by design and/or characterization and is not tested in production.
- (6) Number specified is the slower of positive and negative slew rates.
- (7) Specified by characterization only.



TYPICAL CHARACTERISTICS

NOTE: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only. At $T_A = +25^\circ\text{C}$, $V_S = 5\text{V}$, $R_L = 10\text{k}\Omega$, REF connected to $V_S/2$, $V_{OUT} = V_S/2$, unless otherwise noted.

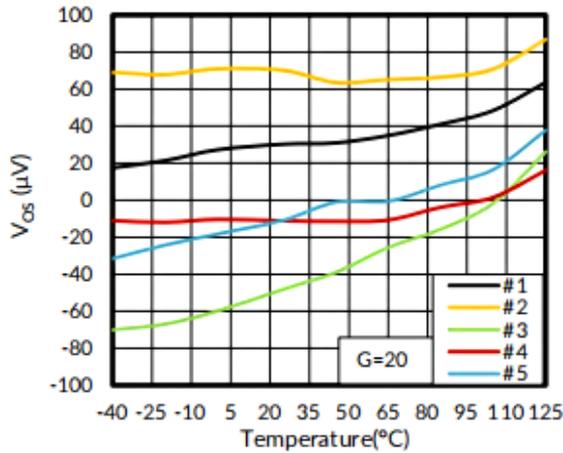


Figure 1. Offset Voltage vs Temperature

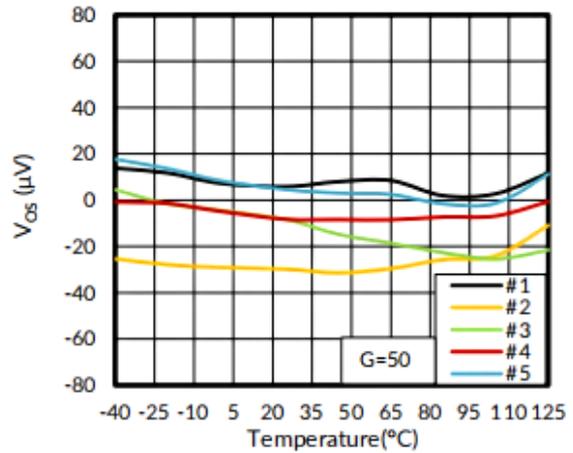


Figure 2. Offset Voltage vs Temperature

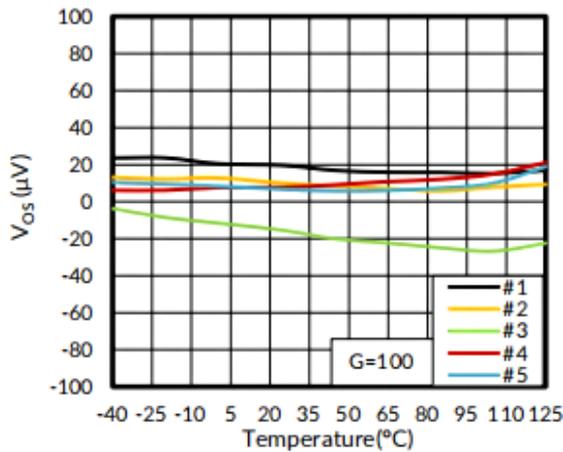


Figure 3. Offset Voltage vs Temperature

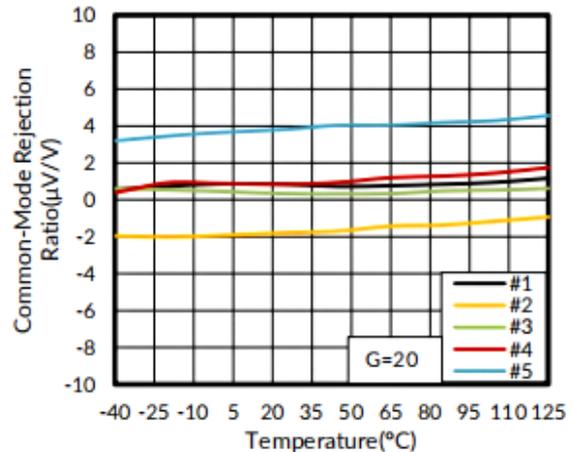


Figure 4. Common-Mode Rejection Ratio vs Temperature

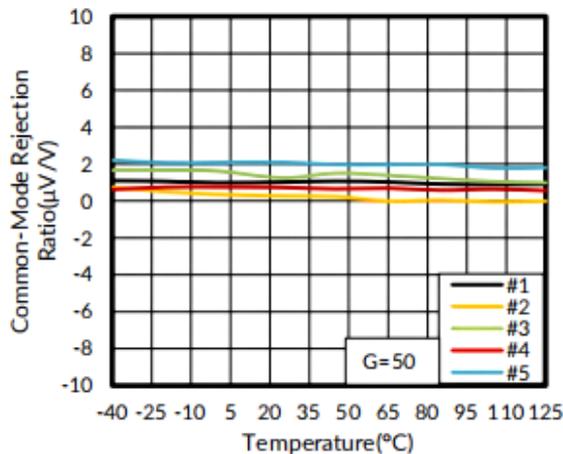


Figure 5. Common-Mode Rejection Ratio vs Temperature

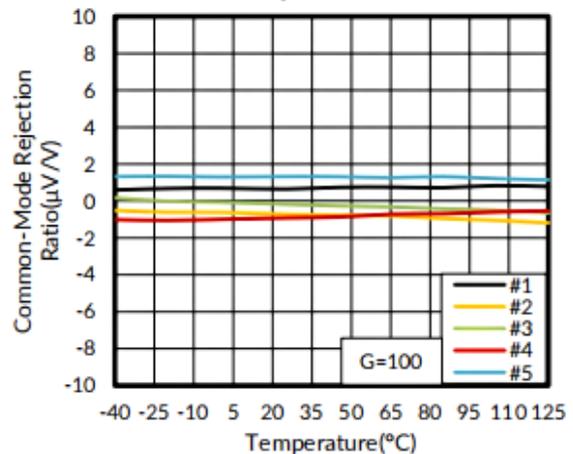


Figure 6. Common-Mode Rejection Ratio vs Temperature

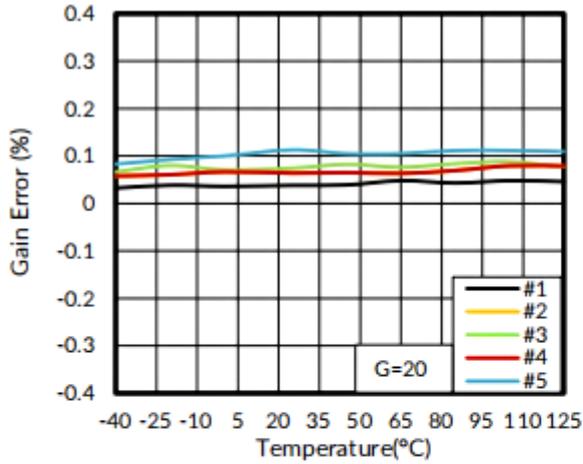


Figure 7. Gain Error vs Temperature

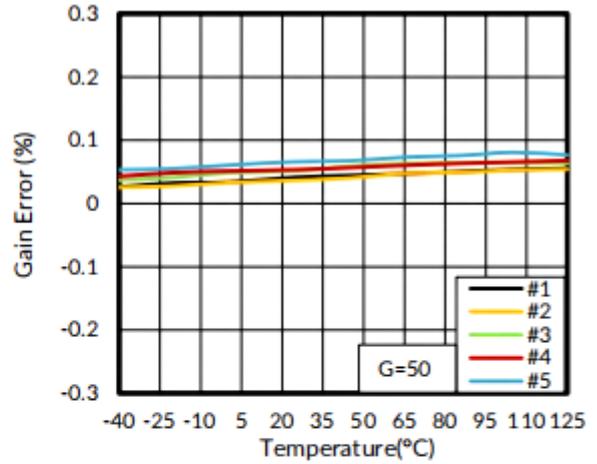


Figure 8. Gain Error vs Temperature

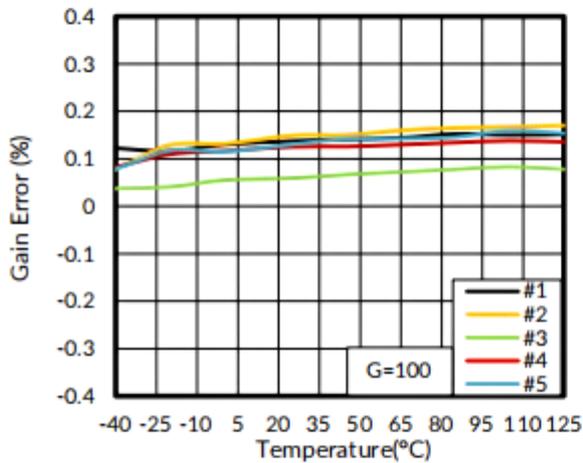


Figure 9. Gain Error vs Temperature

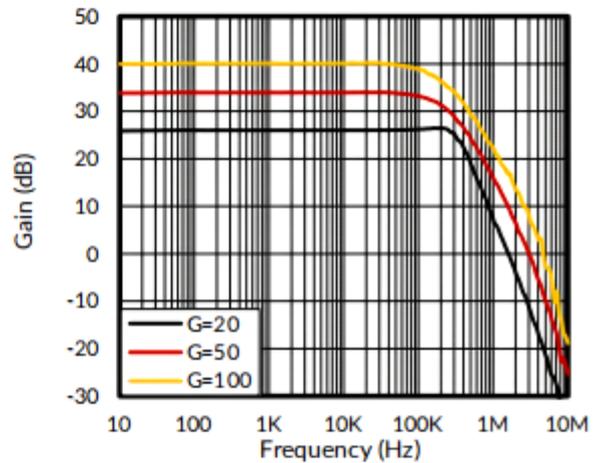


Figure 10. Gain vs Frequency

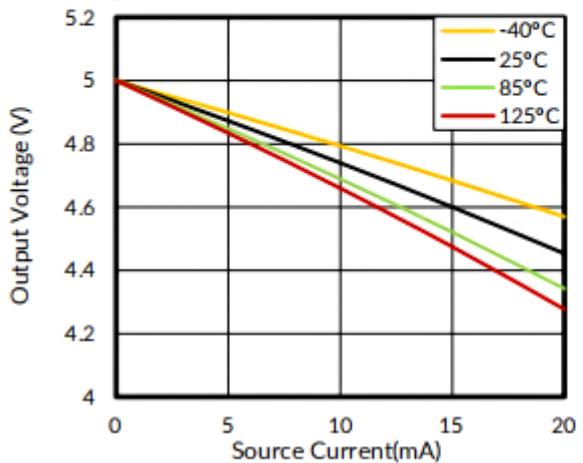


Figure 11. Output Voltage vs Source Current

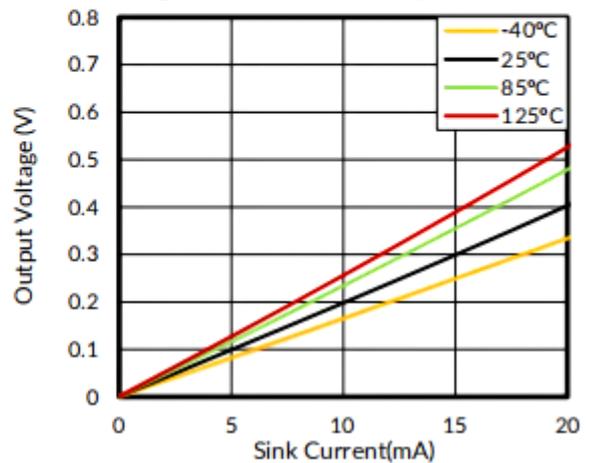


Figure 12. Output Voltage vs Sink Current

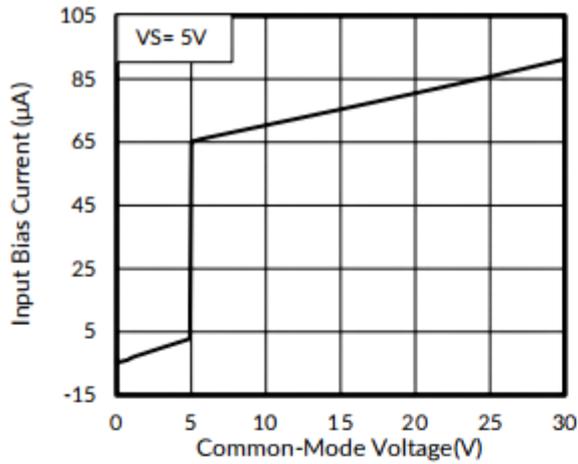


Figure 13. Input Bias Current vs Common-Mode Voltage

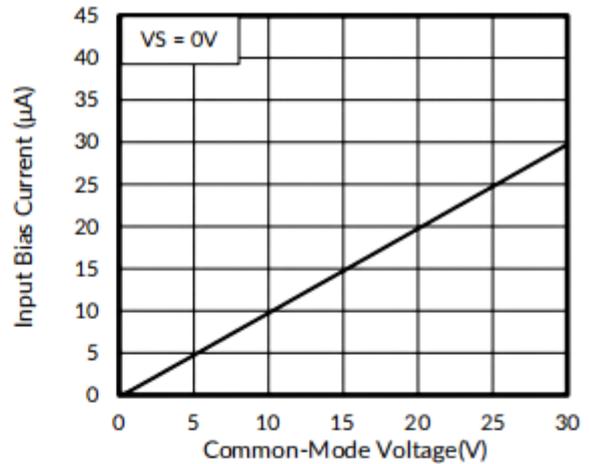


Figure 14. Input Bias Current vs Common-Mode Voltage (Both Inputs, Shutdown)

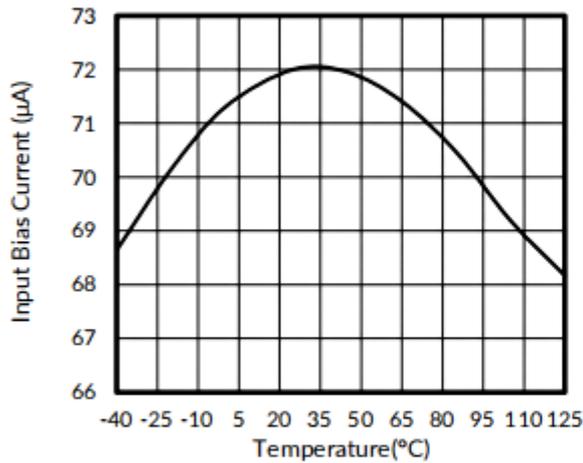


Figure 15. Input Bias Current vs Temperature

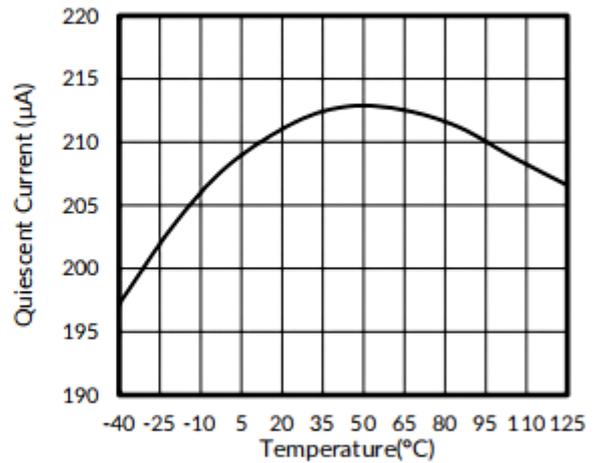


Figure 16. Quiescent Current vs Temperature

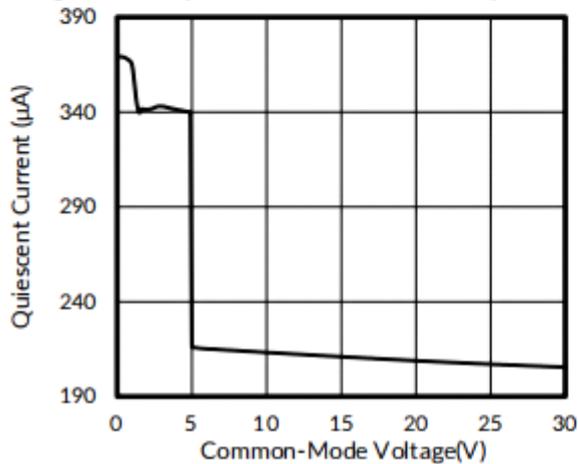


Figure 17. Quiescent Current vs Common-Mode Voltage

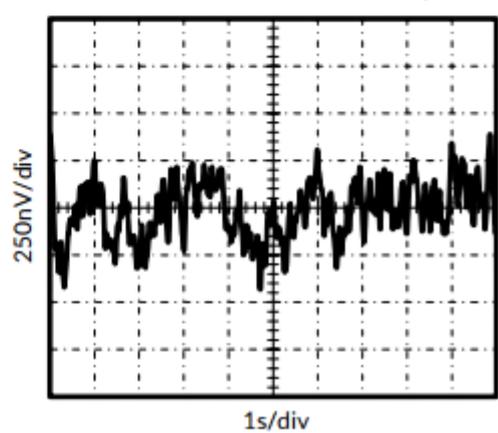


Figure 18. 0.1Hz to 10Hz Voltage Noise (Referred-to-Input)

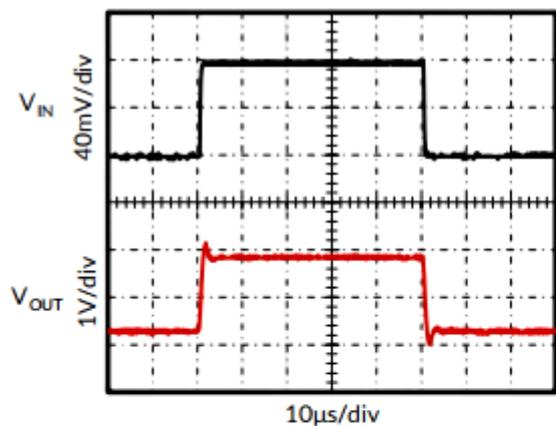


Figure 19. Step Response

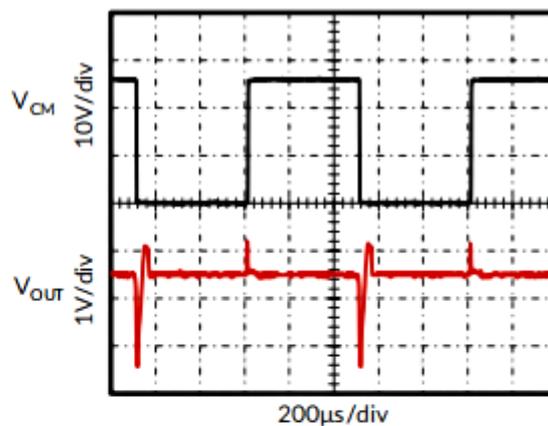


Figure 20. Common-Mode Voltage Transient Response

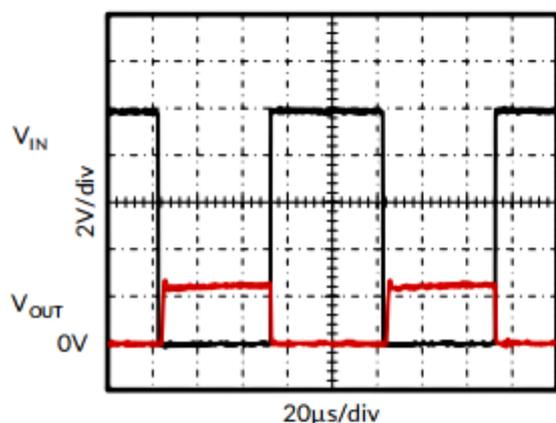


Figure 21. Inverting Differential Input Overload

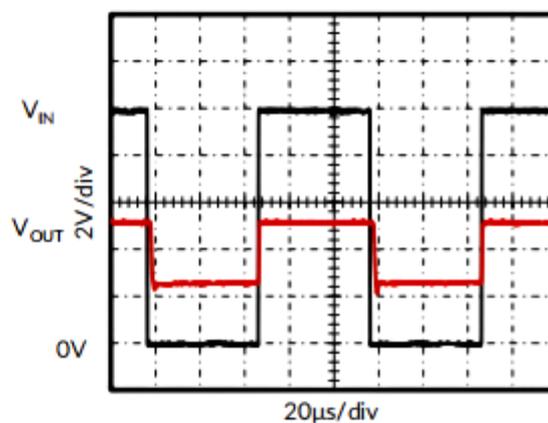


Figure 22. Noninverting Differential Input Overload

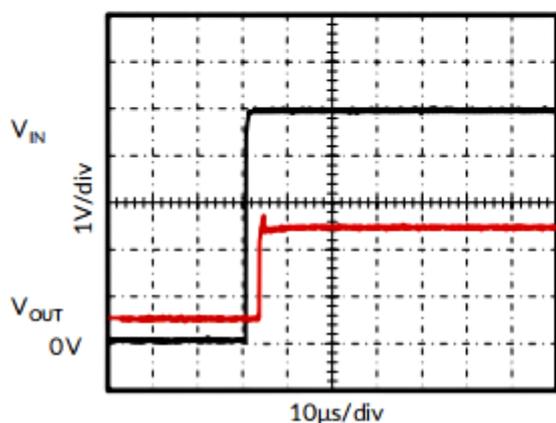


Figure 23. Start-Up Response

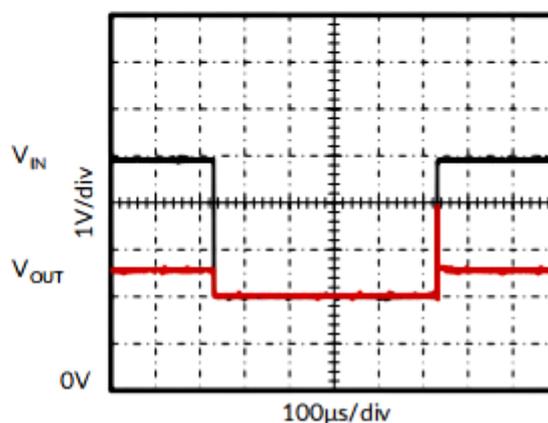


Figure 24. Brownout Recovery



APPLICATION NOTES

Information in the following applications sections is not part of the AOS component specification, and AOS does not warrant its accuracy or completeness. AOS's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

Power Supply

The input circuit of the AOS181 can accurately measure beyond its power supply voltage. For example, the V_{CC} power supply can be 5V, whereas the load power supply voltage can be as high as 30V. The output voltage range of the OUT terminal is limited by the voltages on the power supply pin. Note also that the AOS181 can withstand the full -0.1V to 30V range in the input pins, regardless of whether the device has power applied or not.

Selecting RSHUNT

The AOS181 series of current shunt monitors give high accuracy at a full-scale range on the order of 10mV. This accuracy reduces shunt dissipation by an order of magnitude with many additional benefits. Alternatively, there are applications that must measure current over a wide dynamic range that can take advantage of the low offset on the low end of the measurement. Most often, these applications can use gain of 20, 50 or 100 to accommodate larger shunt drops on the upper end of the scale.

Unidirectional Operation

Unidirectional operation allows the AOS181 to measure currents through a resistive shunt in one direction. The most frequent case of unidirectional operation sets the output at ground by connecting the REF pin to ground.

In unidirectional applications where the highest possible accuracy is desirable at very low inputs, bias the REF pin to a convenient value above 300mV to get the device output swing into the linear range for zero inputs.

A less frequent case of unipolar output biasing is to bias the output by connecting the REF pin to the supply; in this case, the quiescent output for zero input is at quiescent supply. This configuration would only respond to negative currents (inverted voltage polarity at the device input).

Shutting Down the AOS181 Series

While the AOS181 series do not have a shutdown pin, the low power consumption allows powering from the output of a logic gate or transistor switch that can turn on and turn off the AOS181 power supply quiescent current.

However, in current shunt monitoring applications, there is also a concern for how much current is drained from the shunt circuit in shutdown conditions. Evaluating this current drain involves considering the simplified schematic of the AOS181 in shutdown mode shown in Figure 25.

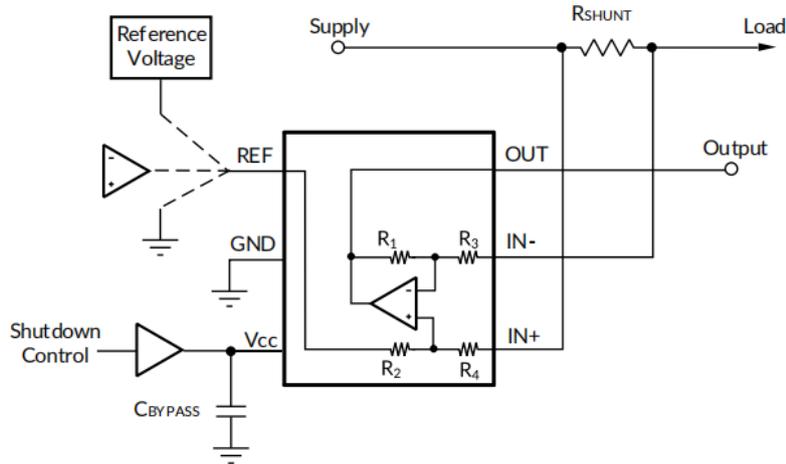


Figure 25. Basic Circuit for Shutting Down RS181 with Grounded Reference

REF Input Impedance Effects

As with any difference amplifier, the AOS181 series common mode rejection ratio is affected by any impedance present at the REF input. This concern is not a problem when the REF pin is connected directly to most references or power supplies. When using resistive dividers from the power supply or a reference voltage, the REF pin should be buffered by an operational amplifier.

In systems where the AOS181 output can be sensed differentially, such as by a differential input ADC or by using two separate ADC inputs, the effects of external impedance on the REF input can be cancelled. Figure 26 depicts a method of taking the output from the AOS181 by using the REF pin as a reference.

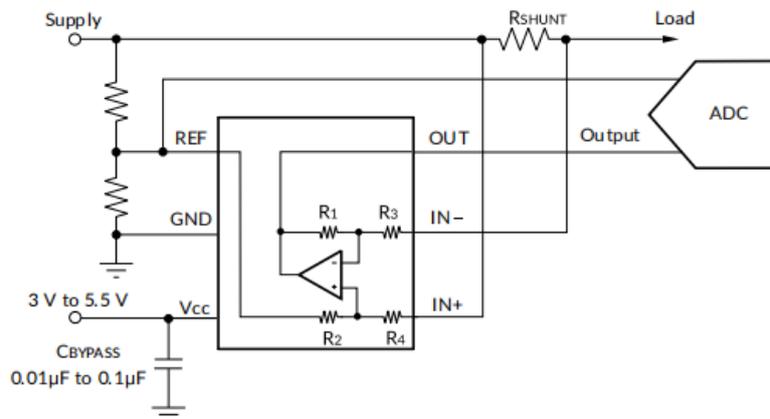


Figure 26. Sensing RS181 to cancel effects of impedance on the REF input



Using the AOS181 with Common Mode Transients above 30V

With a small amount of additional circuitry, the AOS181 series can be used in circuits subject to transients higher than 30V, such as automotive applications. Use only Zener diode or Zener-type transient absorbers (sometimes referred to as Transzorbs); any othertype of transient absorber has an unacceptable time delay. Start by adding a pair of resistors as shown in Figure 27 as working impedance for the Zener. It is desirable to keep these resistors as small as possible, most often around 10 . Larger values can be used with an effect on gain that is discussed in the section on input filtering.

Because this circuit limits only short-term transients, many applications are satisfied with a 10 resistor along with conventional Zener diodes of the lowest power rating that can be found. This combination uses the least amount of board space.

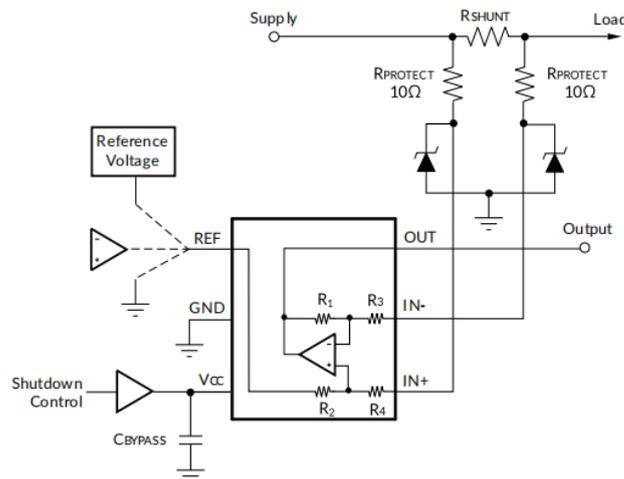
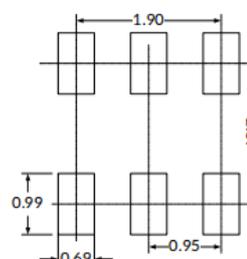
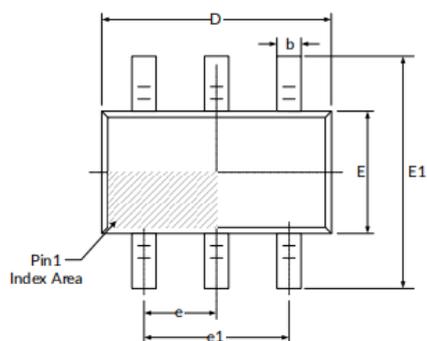


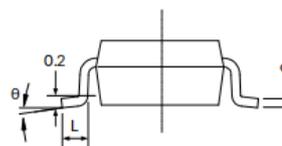
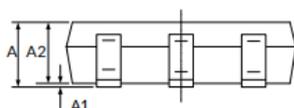
Figure 27. Transient Protection Using Dual Zener Diodes



PACKAGE OUTLINE DIMENSIONS
SOT23-6⁽³⁾



RECOMMENDED LAND PATTERN (Unit: mm)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A ⁽¹⁾	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D ⁽¹⁾	2.820	3.020	0.111	0.119
E ⁽¹⁾	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC) ⁽²⁾		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
	0°	8°	0°	8°

NOTE:

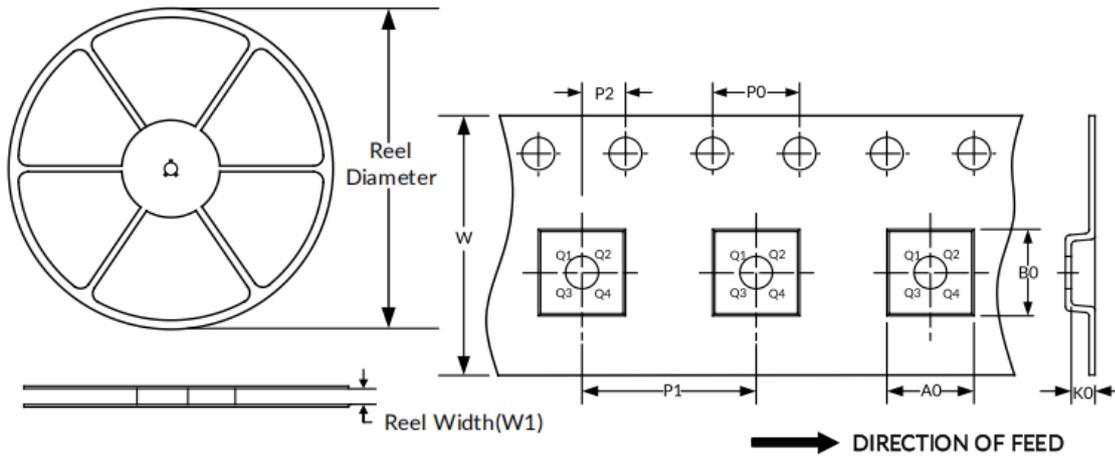
1. Plastic or metal protrusions of 0.15mm maximum per side are not included.
2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
3. This drawing is subject to change without notice.



TAPE AND REEL INFORMATION

REEL DIMENSIONS

TAPE DIMENSION



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOT23-6	7''	9.5	3.17	3.23	1.37	4.0	4.0	2.0	8.0	Q3

NOTE:

1. All dimensions are nominal.
2. Plastic or metal protrusions of 0.15mm maximum per side are not included.