AOS SEMICONDUCTOR

产品规格说明书

Product Data Sheet

AOS521XF

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3.6MHz, Rail-to-Rail I/O CMOS Operational Amplifier

DESCRIPTIONS

The AOS52X families of products offer low voltage operation and rail-to-rail input and output, as well as excellent speed/power consumption ratio, providing an excellent bandwidth (3.6MHz) and slew rate of 1.8V/us. The op-amps are unity gain stable and feature an ultra-low input bias current.

The devices are ideal for sensor interfaces, active filters and portable applications. The AOS521S, AOS522S include a shutdown mode. Under logic control, the amplifiers can be switched from normal operation to a standby current that is less than 1uA.The AOS52X families of operational amplifiers are specified at the full temperature range of −40℃ to +125℃ under single or dual power supplies of 2.5V to 5.5V.

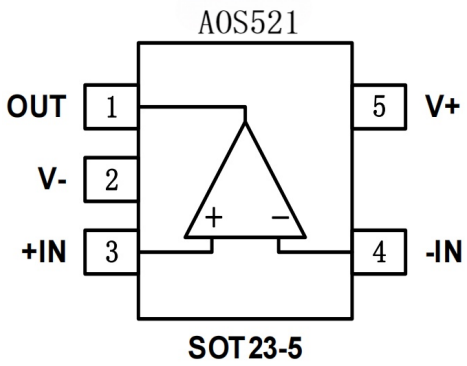
FEATURES

★ GHIGH GAIN BANDWIDTH:3.6MHz
★ RAIL-TO-RAIL INPUT AND OUTPUT 0.8mV Typical Vos
★ INPUT VOLTAGE RANGE: -0.1V to +5.6V with Vs = 5.5V
★ SUPPLY RANGE: +2.5V to +5.5V
★ SHUTDOWN: AOS521S/AOS522S
★ SPECIFIED UP TO +125℃
★ Micro SIZE PACKAGES: SOT23-5, SOT23-6

APPLICATIONS

★ SENSORS
★ PHOTODIODE AMPLIFICATION
★ ACTIVE FILTERS
★ TEST EQUIPMENT
★ DRIVING A/D CONVERTERS

PIN CONFIGURATIONS



SOT23-5

ABSOLUTE MAXIMUM RATINGS (1)

Supply Voltage, V+ to V-	7.0V
Input Terminals, Voltage (2)	– 0.5 to (V+) + 0.5V
Current (2)	±10mA
Storage Temperature	−65°C to +150°C
Operating Temperature	−40°C to +125°C
Junction Temperature	150°C
Package Thermal Resistance @ T_A = +25°C	
SOT23-5, SOT23-6	200°C/W
MSOP-10, SOIC-8	150°C/W
SOIC-14, TSSOP-14	100°C/W
Lead Temperature (Soldering, 10s)	260°C
ESD Susceptibility	
HBM	5000V
MM	400V

(1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. These are stress ratings only , and functional operation of the device at these or any other conditions beyond those specified is not implied.
(2) Input terminals are diode-clamped to the power-supply rails. Input signals that can swing more than 0.5V beyond the supply rails should be current-limited to 10mA or less.

ESD SENSITIVITY CAUTION

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

PACKAGE/ORDERING INFORMATION

PRODUCT	ORDERING NUMBER	TEMPERATURE RANGE	PACKAGE LEAD	PACKAGE MARKING	PACKAGEOPTION
AOS521	AOS521XF	-40°C~125°C	SOT23-5	AOS521	Tape and Reel,3000

ELECTRICAL CHARACTERISTICS

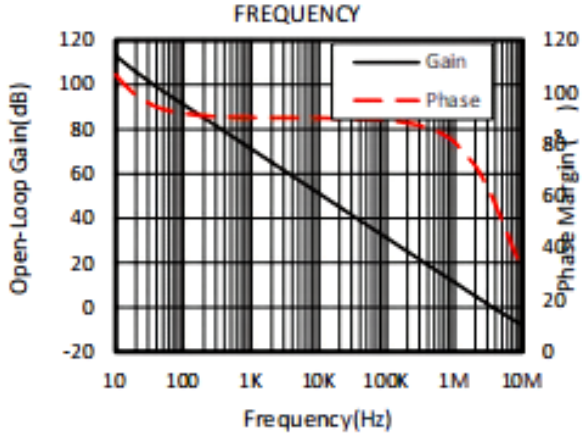
(At T_A= +25℃, V_S=5V, R_L= 10kΩ connected to V_S/2, and V_{OUT}= V_S/2, unless otherwise noted.)

PARAMETER		CONDITIONS	T_J	AOS521			UINTS
				MIN	TYP	MAX	
POWER SUPPLY							
Vs	Operating Voltage Range		25°C	2.5		5.5	V
IQ	Quiescent Current/ Amplifier		25°C		260	350	uA
PSRR	Power-Supply Rejection Ratio	Vs=2.5V to 5.5V, Vcm=(V-)+0.5V	25°C	76	86		dB
			-40°C to 125°C	69			
INPUT							
Vos	Input Offset Voltage		25°C		0.8	3	mV
VosTC	Input Offset VoltageAverage Drift	-40°C to 125°C			2		uV/°C
IB	Input Bias Current		25°C		1	10	pA
Ios	Input Offset Current		25°C		1	10	pA
Vcm	Common-Mode Voltage Range	Vs= 5.5V	25°C	-0.1		5.6	V
CMRR	Common-ModeRejection Ratio	Vs= 5.5V, Vcm=-0.1V to 4V	25°C	76	87		dB
			-40°C to 125°C	71			
		Vs= 5.5V, Vcm=-0.1V to 5.6V	25°C	62	71		
			-40°C to 125°C	60			
OUTPUT							
A_{OL}	Open-Loop Voltage Gain	R_L=2KΩ, Vo= 0.15V to 4.85V	25°C	100	107		dB
			-40°C to 125°C	86			
		R_L=10KΩ ,Vo=0.05V to 4.95V	25°C	100	110		
			-40°C to 125°C	87			
	Output Swing From Rail	R_L=2KΩ	25°C		31		mV
		R_L=10KΩ			7		
Iout	Output Short-Circuit Current		25°C		80		mA
FREQUENCY RESPONSE							
SR	Slew Rate		25°C		1.8		V/us
GBP	Gain-Bandwidth Product		25°C		3.6		MHz
PM	Phase Margin		25°C		65		°
ts	Setting Time,0.1%				0.5		us
	Overload Recovery Time	V_{IN} · Gain≥VS			0.7		us
NOISE							
en	Input Voltage Noise Density	f = 1KHz	25°C		15		nV/√Hz
		f = 10KHz	25°C		13		nV/√Hz
ENABLE/SHUTDOWN(AOS521S,AOS522S)							
$I_{Q(OFF)}$	Supply Current in Shutdown		25°C		<1		uA
t_{OFF}			25°C		3		us
t_{ON}			25°C		20		us
V_L	Shut Down		25°C	V-		(V-) +0.8	V
V_H	Amplifier Is Active		25°C	(V-) +2		V+	V

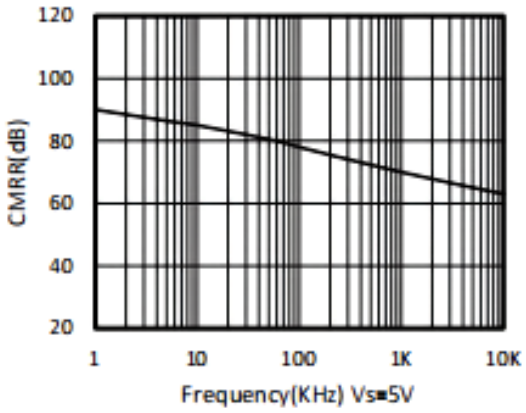
TYPICAL CHARACTERISTICS

At T_A= +25℃, Vs=5V, R_L= 10kΩ connected to Vs/2, V_{OUT}= Vs/2, unless otherwise noted.

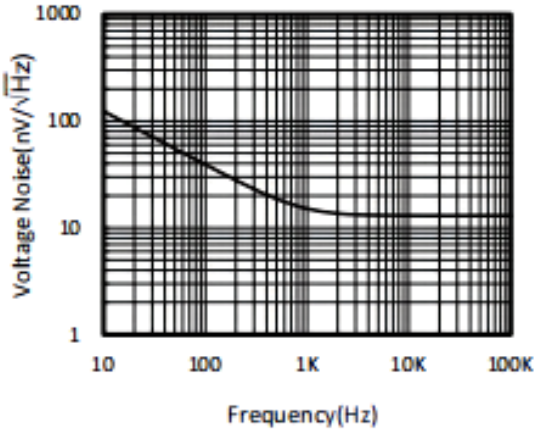
OPEN-LOOP GAIN AND PHASE VS FREQUENCY



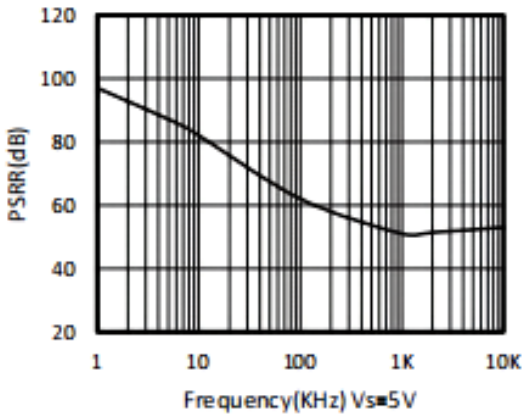
COMMON-MODE REJECTION RATIO vs FREQUENCY



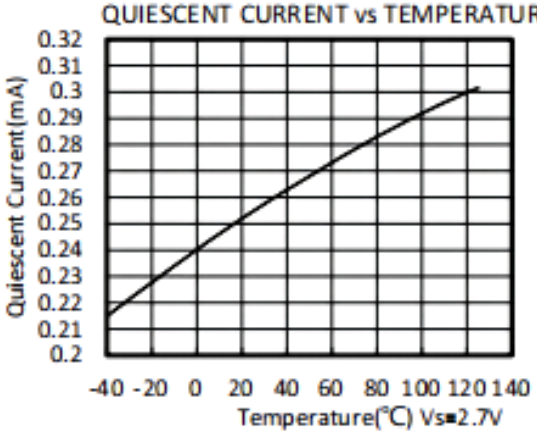
INPUT VOLTAGE NOISE SPECTRAL DENSITY vs FREQUENCY



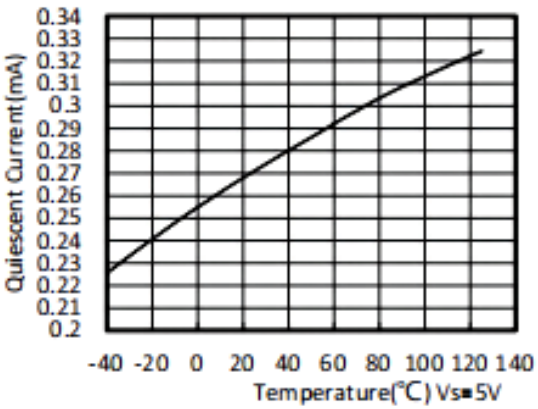
POWER-SUPPLY REJECTION RATIO vs FREQUENCY



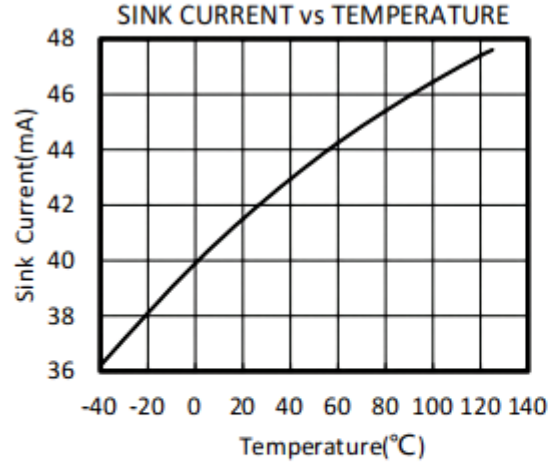
QUIESCENT CURRENT vs TEMPERATURE



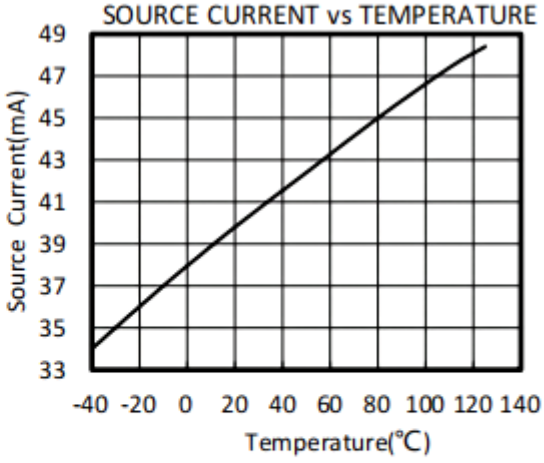
QUIESCENT CURRENT vs TEMPERATURE



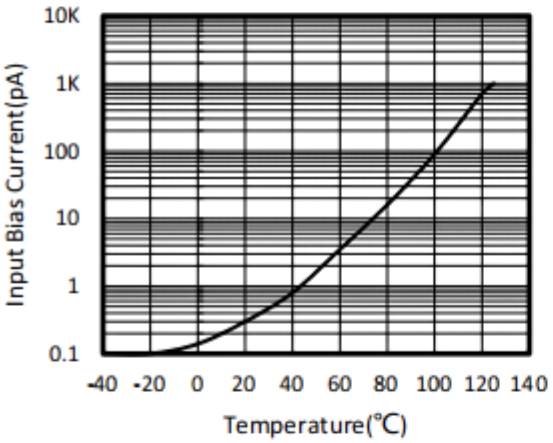
SINK CURRENT vs TEMPERATURE



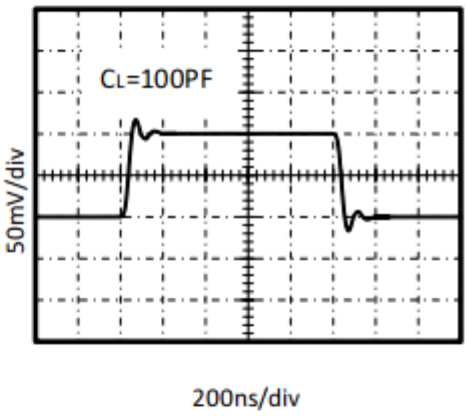
SOURCE CURRENT vs TEMPERATURE



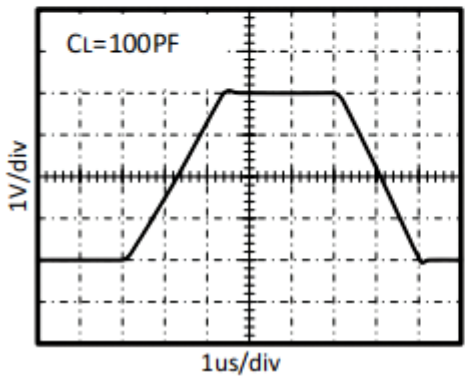
INPUT BIAS CURRENT vs TEMPERATURE



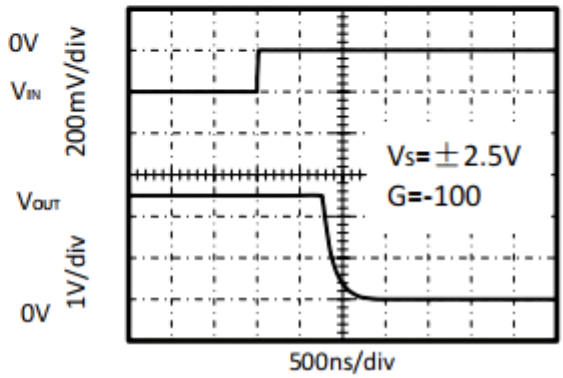
SMALL-SIGNAL STEP RESPONSE



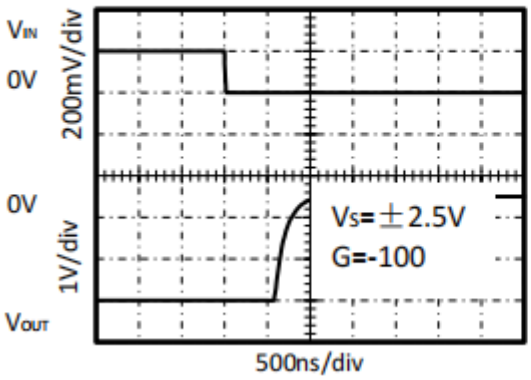
LARGE-SIGNAL STEP RESPONSE



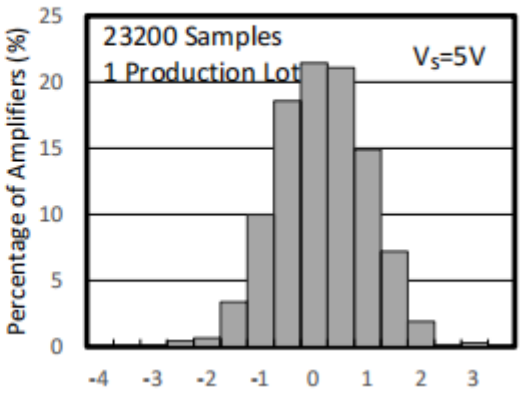
POSITIVE OVERLOAD RECOVERY



NEGATIVE OVERVOLTAGE RECOVERY



OFFESET VOLTAGE PRODUCTION DISTRIBUTION



APPLICATION NOTES

The AOS521, AOS522, AOS524, AOS521S, AOS522S are high precision, rail-to-rail operational amplifiers that can be run from a single-supply voltage 2.5V to 5.5V(±1.25V to ±2.75V). Supply voltages higher than 7V (absolute maximum) can permanently damage the amplifier.

Rail-to-rail input and output swing significantly increases dynamic range, especially in low-supply applications.

Good layout practice mandates use of a 0.1uF capacitor place closely across the supply pins.

AOS521S/AOS522S ENABLE FUNCTION

The AOS521S/AOS522S includes a shutdown mode. Under logic control, the amplifiers can beswitched from normal mode to a standby current of 1uA. When the Enable pin is connected to high, the amplifier is active. Connecting Enable low disables the amplifier, and places the amplifier, and place the output in a high-impedance state.

LAYOUT GUIDELINS

Attention to good layout practices is always recommended. Keep traces short. When possible, use a PCB ground plane with surface-mount components placed as close to the device pins as possible. Place a 0.1uF capacitor closely across the supply pins.

These guidelines should be applied throughout the analog circuit to improve performance and provide benefits such as reducing the EMI susceptibility.

INSTRUMENTATION AMPLIFIER

In the three-op amp, instrumentation amplifier configuration shown in Figure 2

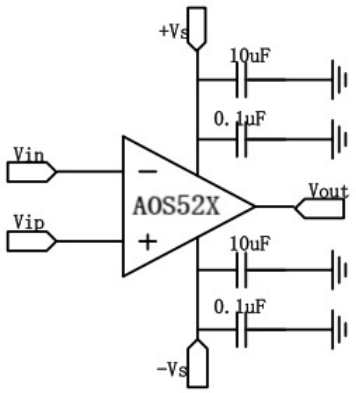


Figure1. Amplifier with Bypass Capacitors

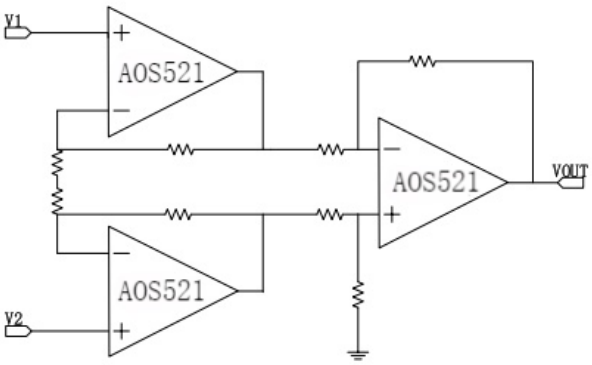
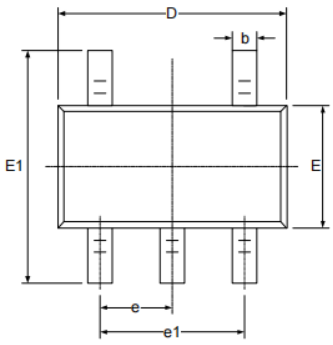
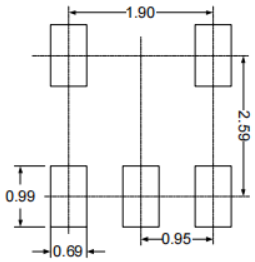


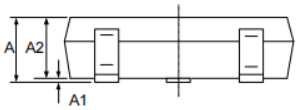
Figure2. Amplifier instrumentation amplifier

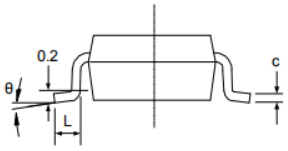
PACKAGE OUTLINE DIMENSIONS
SOT23-5





RECOMMENDED LAND PATTERN (Unit: mm)





Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0 °	8 °	0 °	8 °