



AOS
SEMICONDUCTOR

产品规格说明书

Product Data Sheet

AOSH4093XP

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电源管理IC



通信接口芯片



二三极管



LDO稳压器



逻辑器件



MOSFETs



运算放大器



显示驱动



MCU单片机



光电器件



DESCRIPTION

The AOSH4093 is a quad 2-input NAND gate with Schmitt-trigger inputs. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of Vcc. Schmitt trigger inputs transform slowly changing input signals into sharply defined jitter-free output signals. The AOSH4093 is available in Green SOP14 packages. It operates over an ambient temperature range of -40°C to 125°C.

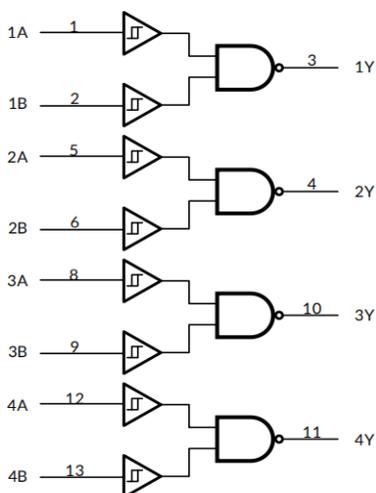
FEATURES

- Schmitt Trigger Input Discrimination
- Fully Static Operation
- 5V, 10V, and 15V Parametric Ratings
- Wide Supply Voltage Range from 3V to 15V
- CMOS Low Power Dissipation
- High Noise Immunity
- Standardized Symmetrical Output Characteristics
- Operating Temperature Range: -40°C to 125°C
- Micro Size Packages: SOP14

APPLICATIONS

- Wave and Pulse Shapers
- Astable Multivibrators

Functional Block Diagram



Device Information (1)

PART NUMBER	PACKAGE	BODY SIZE (NOM)
AOSH4093	SOP14	8.65mm × 3.90mm

(1)For all available packages, see the orderable addendum at the end of the data sheet.

FUNCTION TABLE

INPUTS		OUTPUT
nA	nB	nY
L	L	H
L	H	H
H	L	H
H	H	L

H=High Voltage Level
L=Low Voltage Level



PACKAGE/ORDERING INFORMATION

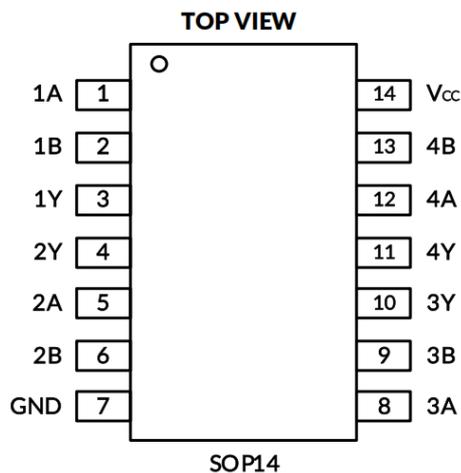
PRODUCT	ORDERING NUMBER	TEMPERATURE RANGE	PACKAGE LEAD	PACKAGE MARKING ⁽²⁾	MSL ⁽³⁾	PACKAGE OPTION
AOSH4093	AOSH4093XP	-40°C ~+125°C	SOP14	AOSH4093	MSL3	Tape and Reel , 4000

NOTE:

- (1) This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the right-hand navigation.
- (2) There may be additional marking, which relates to the lot trace code information (data code and vendor code), the logo or the environmental category on the device.
- (3) MSL, The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications.



PIN CONFIGURATIONS



PIN DESCRIPTION

PIN	NAME	I/O TYPE ⁽¹⁾	FUNCTION
SOP14			
1	1A	I	Input
2	1B	I	Input
3	1Y	O	Output
4	2Y	O	Output
5	2A	I	Input
6	2B	I	Input
7	GND	P	Ground(0V)
8	3A	I	Input
9	3B	I	Input
10	3Y	O	Output
11	4Y	O	Output
12	4A	I	Input
13	4B	I	Input
14	Vcc	P	Supply vol tage

(1) I=input, O=output, P=power.



Specifications

Absolute Maximum Ratings

Over operating free-air temperature range (unless otherwise noted)⁽¹⁾⁽²⁾

SYMBOL	PARAMETER		MIN	MAX	UNIT
V _{CC}	Supply voltage range		-0.5	18	V
V _I	Input voltage range ⁽²⁾		-0.5	V _{CC} +0.5	
V _O	Voltage range applied to any output in the high-impedance or power-off state ⁽²⁾		-0.5	18	
V _O	Voltage range applied to any output in the high or low state ⁽²⁾⁽³⁾		-0.5	V _{CC} +0.5	
I _{IK}	Input clamp current	V _I <-0.5V		-50	mA
I _{OK}	Output clamp current	V _O <0 or V _O >V _{CC} +0.5V		± 50	
I _O	Continuous output current			± 25	mA
	Continuous current through V _{CC} or GND			± 50	
J _A	Package thermal impedance ⁽⁴⁾	SOP14		105	°C/W
T _J	Junction temperature ⁽⁵⁾		-65	150	°C
T _{stg}	Storage temperature		-65	150	

- (1) Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions* is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- (2) The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.
- (3) The value of V_{CC} is provided in the *Recommended Operating Conditions* table.
- (4) The package thermal impedance is calculated in accordance with JESD-51.
- (5) The maximum power dissipation is a function of T_{J(MAX)}, R_{JA}, and T_A. The maximum allowable power dissipation at any ambient temperature is P_D = (T_{J(MAX)} - T_A) / R_{JA}. All numbers apply for packages soldered directly onto a PCB.



ESD Ratings

The following ESD information is provided for handling of ESD-sensitive devices in an ESD protected area only.

			VALUE	UNIT
$V_{(ESD)}$	Electrostatic discharge	Human-body model (HBM), MIL-STD-883K METHOD 3015.9	± 2000	V
		Charged-device model (CDM), ANSI/ESDA/JEDEC JS-002-2018	± 1000	
		Machine Model (MM), JESD22-A115C(2010)	± 200	



ESD SENSITIVITY CAUTION

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.



ELECTRICAL CHARACTERISTICS

over recommended operating free-air temperature range (TYP values are at $T_A = +25^{\circ}\text{C}$, Full=-40°C to +125°C, unless otherwise noted.) ⁽¹⁾

Recommended Operating Conditions

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	MAX	UNIT
Supply voltage	V_{CC}		3	15	V
Input voltage	V_i		0	15	V
Output voltage	V_o		0	V_{CC}	V
Operating temperature	T_A		-40	125	°C

(1)All unused inputs of the device must be held at V_{CC} or GND to ensure proper device operation.



DC Characteristics

PARAMETER		TEST CONDITIONS	V _{CC}	TEMP	MIN ⁽²⁾	TYP ⁽³⁾	MAX ⁽²⁾	UNIT	
V _{T+}	Positive Going Input Threshold Voltage		5V	Full	2		3.5	V	
			10V		3.7		7		
			15V		4.9		11		
V _{T-}	Negative Going Input Threshold Voltage		5V	Full	1.5		3	V	
			10V		3		6.3		
			15V		4		10		
V _T	Hysteresis (V _{T+} -V _{T-})		5V	Full	0.5			V	
			10V		0.7				
			15V		0.9				
V _{OH}			3V to 15V	Full	V _{CC} -0.1			V	
					I _{OH} = -10μA	4.6			
					I _{OH} = -0.36mA	4.55			
			I _{OH} = -0.5mA		5V	4.5			
			I _{OH} = -0.64mA			4.3			
			I _{OH} = -1.1mA			4.15			
			I _{OH} = -1.4mA		10V	4			
			I _{OH} = -1.7mA			9.55			
			I _{OH} = -0.9mA			9.35			
			I _{OH} = -1.3mA		15V	9.2			
			I _{OH} = -1.6mA			14.45			
			I _{OH} = -2.4mA			14.2			
			I _{OH} = -3.4mA			14			
I _{OH} = -4.2mA									
V _{OL}			3V to 15V	Full			0.1	V	
					I _{OL} = 0.36mA				0.4
					I _{OL} = 0.5mA	5V			
			I _{OL} = 0.64mA						0.5
			I _{OL} = 0.9mA		10V				
			I _{OL} = 1.3mA						0.65
			I _{OL} = 1.6mA						0.8
			I _{OL} = 2.4mA		15V				0.55
			I _{OL} = 3.4mA						0.8
			I _{OL} = 4.2mA						1
I _I	A input	V _I =15V or GND	15V	25°C	± 0.1	± 1	μA		
				Full					± 2
I _{CC}		V _I =V _{CC} or GND, I _O =0A	5V	25°C	0.1	1	μA		
				Full					5
			10V	25°C	0.5	1	μA		
				Full					15
			15V	25°C	1	1.5	μA		
				Full					20

- (1) All unused inputs of the device must be held at V_{CC} or GND to ensure proper device operation.
- (2) Limits are 100% production tested at 25°C. Limits over the operating temperature range are ensured through correlations using statistical quality control (SQC) method.
- (3) Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration.



AC Characteristics

PARAMETER	SYMBOL	TEST CONDITIONS	TEMP	MIN ⁽²⁾	TYP ⁽³⁾	MAX ⁽²⁾	UNIT
Propagation Delay	t _{pd}	V _{CC} =5V, C _L =50pF	Full		21.5	38.7	ns
		V _{CC} =10V, C _L =50pF			11.2	19.2	
		V _{CC} =15V, C _L =50pF			9.2	15	
LOW to HIGH Output Transition Time	t _r	V _{CC} =5V, C _L =50pF	Full		21.5	38.4	ns
		V _{CC} =10V, C _L =50pF			11.5	17.2	
		V _{CC} =15V, C _L =50pF			7	13.5	
HIGH to LOW Output Transition Time	t _f	V _{CC} =5V, C _L =50pF	Full		19.5	32.3	ns
		V _{CC} =10V, C _L =50pF			11	18.2	
		V _{CC} =15V, C _L =50pF			8	16.1	
Input Capacitance	C _i	V _{CC} =5V	25°C		4		pF
Power Dissipation Capacitance Per Out	C _{pd}	V _{CC} =5V	f=10MHz	25°C		7.5	pF
		V _{CC} =10V				10	
		V _{CC} =15V				12	

- (1) All unused inputs of the device must be held at V_{CC} or GND to ensure proper device operation.
- (2) This parameter is ensured by design and/or characterization and is not tested in
- (3) Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration.



Typical Characteristics

NOTE: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only.

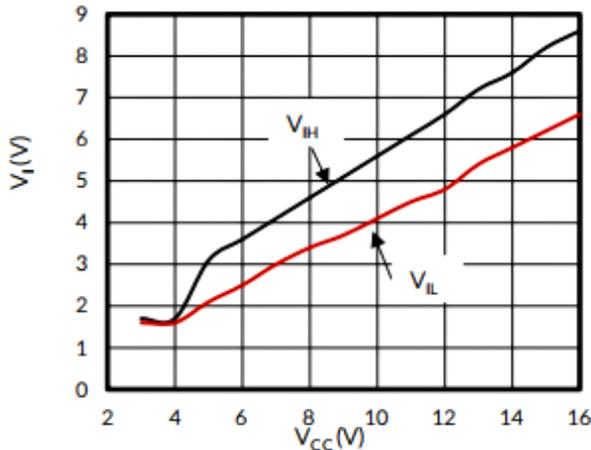


Figure 1. Typical Switching Levels as a Function of Supply Voltage

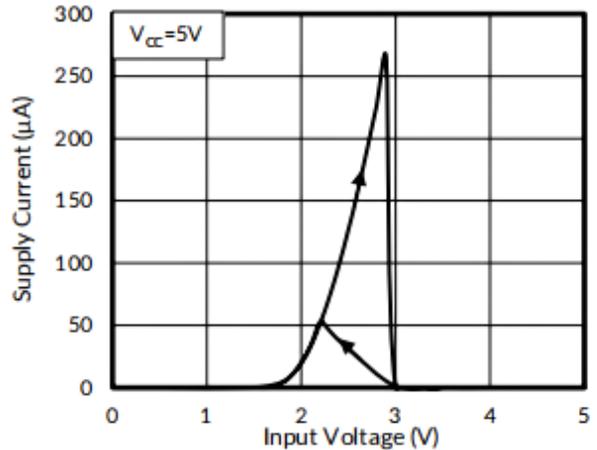


Figure 2. Typical Drain Current as a Function of Input

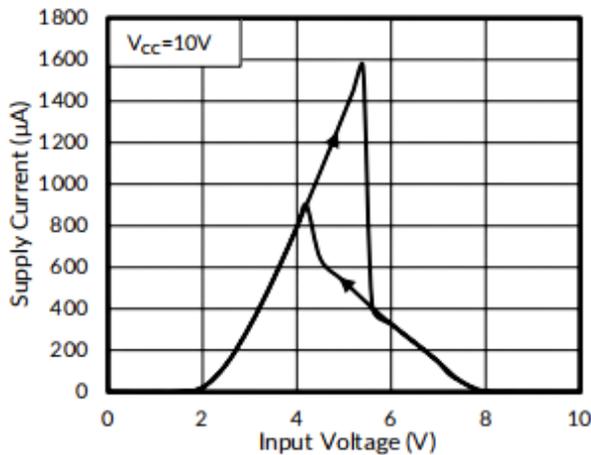


Figure 3. Typical Drain Current as a Function of Input

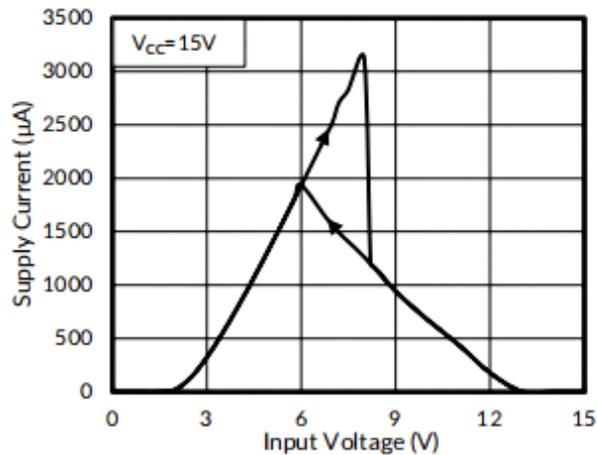
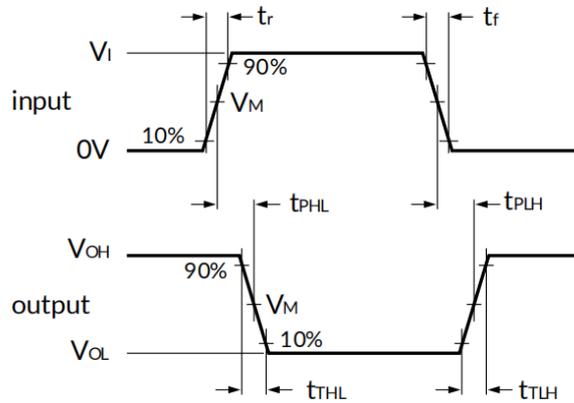


Figure 4. Typical Drain Current as a Function of Input



Parameter Measurement Information



Measurement points are given in Table 1.

Logic levels: VOL and VOH are typical output voltage levels that occur with the output load. tr, tf = input rise and fall times.

Figure 5. Propagation Delay and Output Transition Time
Table 1. Measurement Points

Supply voltage	Input	Output
Vcc	Vm	Vm
5 V to 15 V	0.5 Vcc	0.5 Vcc

Test data given in Table 2.

Definitions for test circuit:

CL = load capacitance including jig and probe capacitance.

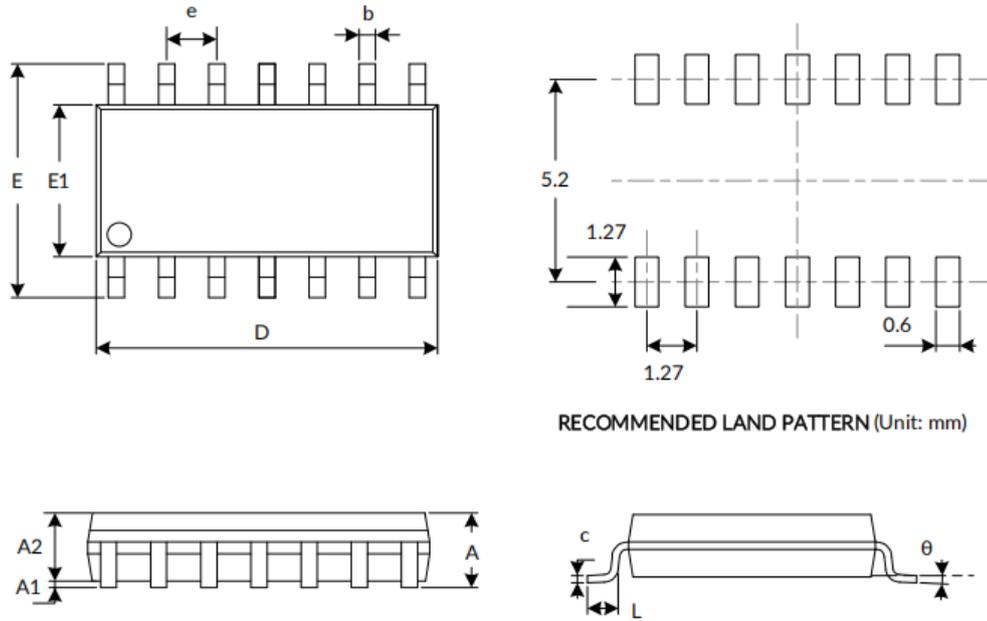
RT= termination resistance should be equal to the output impedance Zo of the pulse generator.

Figure 6. Test Circuit for Measuring Switching Times
Table 2. Test Data

Supply voltage	Input		Load
Vcc	Vi	tr, tf	CL
5 V to 15 V	Vcc or GND	20 ns	50 pF



PACKAGE OUTLINE DIMENSIONS
SOP14⁽³⁾



RECOMMENDED LAND PATTERN (Unit: mm)

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A ⁽¹⁾		1.750		0.069
A1	0.100	0.250	0.004	0.010
A2	1.300	1.500	0.051	0.059
b	0.390	0.470	0.015	0.019
c	0.200	0.240	0.008	0.009
D ⁽¹⁾	8.550	8.750	0.336	0.344
e	1.270(BSC) ⁽²⁾		0.050(BSC) ⁽²⁾	
E	5.800	6.200	0.228	0.244
E1 ⁽¹⁾	3.800	4.000	0.150	0.157
L	0.500	0.800	0.020	0.031
	0°	8°	0°	8°

NOTE:

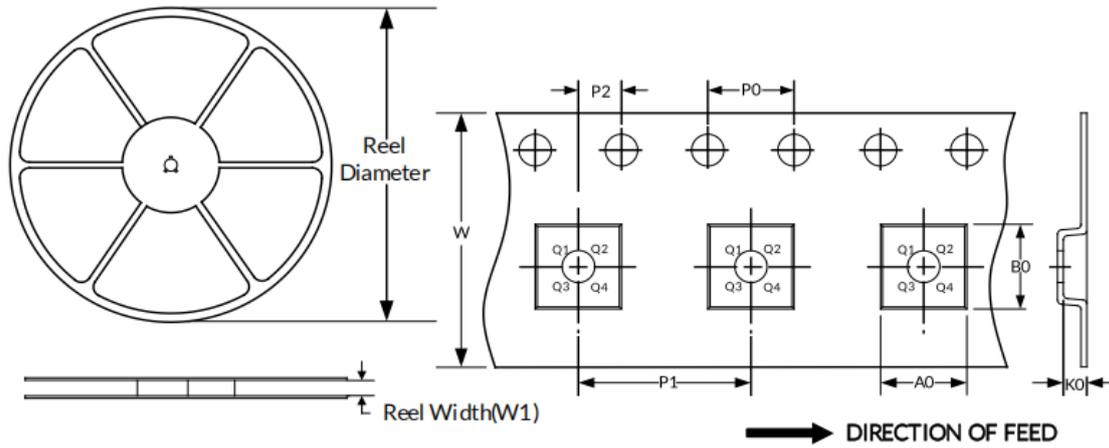
1. Plastic or metal protrusions of 0.15mm maximum per side are not included.
2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
3. This drawing is subject to change without notice.



TAPE AND REEL INFORMATION

REEL DIMENSIONS

TAPE DIMENSION



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width(mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOP14	13''	16.4	6.60	9.30	2.10	4.0	8.0	2.0	16.0	Q1

NOTE:

1. All dimensions are nominal.
2. Plastic or metal protrusions of 0.15mm maximum per side are not included.